## AN IMPROVED ARCHITECTURE FOR NETWORKED CONTROL SYSTEMS

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Abstract: This paper describes a novel networked control systems architecture in which the down-link communication system is designed to operate in two different modes. This achieves improved trade-offs between error probabilities and transmission delays. The method is illustrated for the case of a physical channel which can either function as an error-free delay (virtual) channel or as a delay-free erasure (virtual) channel with a given probability of error. It is shown how this architecture outperforms existing methods which make use of only one communication mode. *Copyright*© 2005 IFAC

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## 1. INTRODUCTION

Networked Control Systems (NCSs), where controller(s) and plant(s) are connected via a communication network have attracted significant attention, see e.g. (Antsaklis and Baillieul, 2004; Tipsuwan and Chow, 2003; Thompson, 2004) and references therein. It turns out that the network itself is a dynamical system, which in general needs to be accounted for in the overall design of an NCS. In particular, digital networks can only transport quantized data and introduce time delays in both the *down-link*, i.e. between controller and actuator nodes, and *up-link*, i.e. between sensor and controller nodes.

In (Quevedo *et al.*, 2004*b*) the idea of sending filtered versions of plant input signals through the down-link channel was investigated. It was shown that this *source coder* design problem is governed by a trade-off between channel utilization (i.e. bitrates used) versus immunity of the NCS with respect to transmission errors.

In particular, if the communication system deployed is such that error free reception can be ensured, then the  $\Delta$ -Modulation based approach proposed in (Quevedo et al., 2003; Goodwin et al., 2004) minimizes bit-rates. In this method, incremental information of plant inputs is sent. At the actuator nodes, plant input signals are reconstructed through integration in the source decoders. The resultant NCS can follow references and compensate for step-like disturbances. One drawback, however, with this source coding method, is that, if transmission errors occur, then performance is degraded significantly. Practical physical channels are always affected by noise, see e.g. (Proakis, 1995; Liu and Yen, 2004; Paulraj et al., 2004). To achieve low error rates, the communication system needs to deploy sophisticated channel coding methods. Their purpose is to overcome the effects of channel noise via introduction of redundancies in the signal transmitted. Larger delays and lower sampling rates are a necessary consequence of these strategies aimed at achieving lower error rates. In a closed loop control setting there is a hard real time requirement and hence these delays are highly undesirable, see e.g. (Tipsuwan and Chow, 2003; Srinivasagupta  $et \ al., 2004$ ).

On the other hand, if faster channel coding methods are deployed, then delays can be kept low and sampling rates high, but random transmission errors are more likely to occur. In this case, the integrating source decoder should be replaced by a stable one, which gives more robustness with respect to transmission errors, as shown in (Quevedo *et al.*, 2004*b*). This is achieved at the expense of increased channel utilization. Also, with a stable source decoder, the reconstructed plant inputs will be artificially restricted in magnitude (due to quantization of all signals transmitted), and regulating capacities of the NCS may be compromised.

To overcome the limitations described above, in the present work, an improved NCS architecture is proposed. In it, the down-link communication system is allowed to operate in two different modes, which are situated at different locations in the performance-complexity spectrum of communication systems for a given physical channel. One mode essentially ensures "error-free transmission", but at the expense of significant time delays and at a reduced sampling rate. The other mode is *faster* (no time delay, higher sampling rate) but is affected by transmission errors.

Within this architecture, a novel controller is developed, which can access both of the "virtual communication channels" described above to send quantized data to the actuator node. This twochannel networked controller is formulated in a finite-set constrained predictive control context, see (Quevedo et al., 2004a) and makes decisions aimed at optimizing performance. As will be shown, the greater flexibility of the architecture can be used to give improved performance, when compared to a one-channel controller which only uses one virtual channel. While this two-channel down-link architecture is applicable to a variety of situations, to fix ideas, we will concentrate here on Ethernet-like networks, which are widely used in practice.

### 2. AN IMPROVED ARCHITECTURE

For ease of exposition a SISO plant G(z) with a unitary sampling interval will be considered. Discrete time will be denoted as  $\ell = 0, 1, 2, \ldots$ . Extensions to multi-actuator systems which share the attention of a single controller, see (Quevedo *et al.*, 2003), present no technical difficulties.

As outlined in the introduction, for a given physical communication channel, there exists a tradeoff between reliability of the overall communication system, and delays introduced and maximum bit-rates allowed, see also (Liu and Yen, 2004; Paulraj *et al.*, 2004). A similar trade-off exists when the communication medium is shared between various users. To regulate this situation, a medium access protocol needs to be used. In case of "Classic Ethernet", which is used in many practical applications, see e.g. (Nilsson, 1998; Tang and de Silva, 2003; Goodwin *et al.*, 2004) and compare to (Thompson, 2004), random time delays are introduced to handle collisions.

As mentioned in (Ling and Lemmon, 2004), if data is excessively delayed, then it is often desirable to purposefully *drop* it, leading to the *erasure channel* model, see also (Tatikonda and Mitter, 2004):

$$\widetilde{u}_E^{\text{out}}(\ell) = (1 - \epsilon(\ell))\widetilde{u}_E(\ell), \quad \ell \in \mathbb{N},$$
 (1)

where  $\epsilon(\ell)$  is an independent random variable which takes the value 1 (corresponding to a dropout) with probability  $\mathcal{P}$  and is equal to 0 otherwise.<sup>1</sup>

If data is allowed to be delayed enough, i.e. a sufficiently large *timeout value*  $\Delta$  is used, and sampling rates are low, then dropouts will be very rare and can be said to essentially not occur<sup>2</sup>. This situation can be modeled via the (noiseless) *delay channel* model:

$$\widetilde{u}_D^{\text{out}}(\ell + \Delta) = \widetilde{u}_D(\ell), \quad \ell \in \mathbb{N}.$$
 (2)

In (2), the channel input  $\tilde{u}_D(\ell)$  is sub-sampled:

$$\widetilde{u}_D(\ell) = 0, \quad \forall (\ell/L) \notin \{\mathbb{N} \cup \{0\}\}, \qquad (3)$$

where  $L \in \mathbb{N}$  is a given sub-sampling factor.

When focusing on Ethernet networks, the twochannel concept described above leads to the down-link architecture illustrated in Fig. 1. As can be seen in that figure, a networked controller utilizes up-link information and a reference signal  $r(\ell)$  to calculate its control actions  $\tilde{u}_D(\ell)$ and  $\tilde{u}_E(\ell)$ . Each of the two virtual channels is equipped with a source decoder, denoted as  $H_D(z)$ and  $H_E(z)$ , which can be tuned following the procedure outlined in (Quevedo *et al.*, 2004*b*).

Note 1. (Relationship to PI-controller). The proposed architecture is conceptually related to a PI-controller implemented in parallel form, see e.g. (Åström and Hägglund, 1995), where the Ppart is fast and is affected by noise while the Ipart has less bandwidth and serves essentially for regulation purposes.

<sup>&</sup>lt;sup>1</sup> This model does not include any delay. It is assumed that small delays are directly incorporated into G(z). More sophisticated erasure-channel models can also be developed, if  $\epsilon(\ell)$  is not assumed independent, but governed by a Markov chain which models the network load.

 $<sup>^2</sup>$  For this, the delay  $\Delta$  should be chosen larger than the so-called "maximum roundtrip time" of the network.

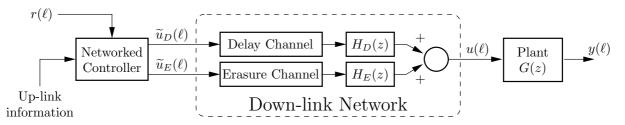


Fig. 1. Two-channel networked control system architecture.

# 3. THE TWO-CHANNEL NETWORKED CONTROLLER

A key point associated with an NCS such as the architecture depicted in Fig. 1 is that the controller needs to satisfy communication constraints imposed by the network.

#### 3.1 Communication Constraints

Since the network is digital, all data sent through the down-link is restricted to be quantized. Moreover, the data sent through the delay channel needs to satisfy the sub-sampling constraint (3). These two communication constraints can be captured via a finite set constraint on the controller outputs:

$$\widetilde{u}(\ell) \triangleq \begin{bmatrix} \widetilde{u}_D(\ell) \\ \widetilde{u}_E(\ell) \end{bmatrix} \in \mathbb{U}(\ell), \quad \ell = 0, 1, 2, \dots$$
(4)

where: 
$$\mathbb{U}(\ell) \triangleq \mathbb{U}_D(\ell) \times \mathbb{U}_E.$$
 (5)

In the above expression,  $\mathbb{U}_E$  is a given finite set and  $\mathbb{U}_D(\ell)$  is defined via:

$$\mathbb{U}_D(\ell) = \begin{cases} \{0\}, & \text{if } (\ell/L) \notin \{\mathbb{N} \cup \{0\}\}, \\ \mathbb{U}_d, & \text{otherwise,} \end{cases}$$
(6)

where  $\mathbb{U}_d$  is a given finite set. Note that  $\mathbb{U}(\ell)$  is *L*-periodic, i.e.:

$$\mathbb{U}(\ell + L) = \mathbb{U}(\ell), \quad \forall \ell \ge 0 \tag{7}$$

and that a controller which respects (4) implicitly incorporates source encoding.

Having established that the networked controller needs to satisfy the constraint (5), finite set constrained receding horizon control (Quevedo etal., 2004a) can be used to optimize performance.

### 3.2 Cost-function

At each time instant  $\ell = k$  the *Two-channel Networked Controller*, minimizes the finite horizon cost function:

$$V_N \triangleq \sum_{\ell=k+1}^{k+N} \|y'(\ell) - r(\ell)\|^2 + \sum_{\ell=k}^{k+N-1} \|u'(\ell) - u_r(\ell)\|_R^2,$$
(8)

which weights predictions of future plant inputs and outputs. In this cost function,  $u_r(\ell)$  is the input reference corresponding to  $r(\ell)$ . The scalar R is the control weighting and  $N \in \mathbb{N}$  is the prediction horizon. Both serve as tuning parameters, see e.g. (Maciejowski, 2002; Mayne *et al.*, 2000).

To minimize  $V_N$ , the following (noise-free) prediction model, see (1), (2) and Fig. 1, is used:

$$y'(\ell) = G(z)u'(\ell)$$
  

$$u'(\ell) = \left[z^{-\Delta}H_D(z) \ H_E(z)\right]\widetilde{u}'(\ell).$$
(9)

In (9) the decision variables satisfy, see (4):

 $\widetilde{u}'(\ell) \in \mathbb{U}(\ell), \quad \forall \ell \in \{k, k+1, \dots, k+N-1\}.$  (10)

Rather than sending the entire optimizing control sequence corresponding to  $\ell \in \{k, k+1, \ldots, k+N-1\}$ , following the Receding Horizon Principle, the controller output is set equal to the first element (corresponding to  $\ell = k$ ). This leads ultimately to the true plant input  $u(\ell)$ . The optimizing procedure is then repeated *ad infinitum*.

Fig. 2 illustrates this idea for a horizon N = 4. Also included in this figure is the constraint set  $\mathbb{U}_D(\ell)$  for a delay channel with sub-sampling factor L = 3, see (6).

$$k = 3: \qquad (\tilde{u}^{*}(3) \cdot \cdot \cdot \cdot)$$

$$k = 2: \qquad (\tilde{u}^{*}(2) \cdot \cdot \cdot)$$

$$k = 1: \qquad (\tilde{u}^{*}(1) \cdot \cdot \cdot \cdot)$$

$$k = 0: (\tilde{u}^{*}(0) \cdot \cdot \cdot \cdot)$$

$$\mathbb{U}_{D}(\ell): \quad \mathbb{U}_{d} \quad \{0\} \quad \{0\} \quad \mathbb{U}_{d} \quad \{0\} \quad \{0\} \quad \mathbb{U}_{d}$$

$$\ell: \quad 0 \quad 1 \quad 2 \quad 3 \quad 4 \quad 5 \quad 6$$

Fig. 2. Receding horizon principle and constraint sets for  $\tilde{u}_D(\ell)$ , given L = 3 and N = 4.

# 4. COMPUTATIONAL ASPECTS

The deployment of two virtual down-link channels certainly increases the complexity of the networked controller when compared to a controller which uses only the erasure channel.<sup>3</sup> However, since the delay channel is down-sampled, the increase of complexity will only be moderate.

 $<sup>^{3}</sup>$  A one-channel controller which uses solely the delay channel will often give very poor performance, see e.g. the simulation studies included in Section 6.

More precisely, to find the optimizing sequence to  $V_N$  in (8), in general an (explicit or implicit) exhaustive enumeration technique will be used. Thus, the on-line computation time of the controller is essentially proportional to the cardinality of the search set of the decision variables. Due to the sub-sampling constraint (6), the effective number of decision variables  $\tilde{u}_D(\ell)$  examined within the horizon  $k \leq \ell < k+N$  depends upon k. This is illustrated in Fig. 2, where at time instants k = 0 and k = 3 two decision variables need to be optimized, while at k = 1 and k = 2, only one variable falls inside the horizon.

An exact expression for the cardinality of the search set is given in Lemma 2 below. It uses the following definition:

Definition 1. (Division with remainder). For any  $N, L \in \mathbb{N}$ , the integers floor (N/L) and rem (N/L) are defined implicitly as:

$$N = L \operatorname{floor} \left( N/L \right) + \operatorname{rem} \left( N/L \right),$$

where:

$$(N/L) - 1 < \text{floor} (N/L) \le N/L,$$
  
 $0 \le \text{rem} (N/L) < L.$ 

Lemma 2. The cardinality of the search set is:

$$N_{\mathbb{U}}(k) = \eta(k) \left( |\mathbb{U}_E| \right)^N \left( |\mathbb{U}_d| \right)^{\text{floor}(N/L)}, \quad (11)$$

where  $|\cdot|$  denotes cardinality,

$$\eta(k) \triangleq \begin{cases} 1, & \text{if } 0 < k \le L - \operatorname{rem}(N/L), \\ |\mathbb{U}_d|, & \text{if } L - \operatorname{rem}(N/L) < k \le L, \end{cases}$$

and

$$\eta(k+L) = \eta(k), \quad \forall k \ge 0.$$

**PROOF.** It follows directly from (5), (6) and (10) that:

$$N_{\mathbb{U}}(k) = (|\mathbb{U}_E|)^N \prod_{k \le \ell < k+N} |\mathbb{U}_D(\ell)|.$$

On the other hand, careful inspection of Fig. 2 yields:

$$\prod_{k \le \ell < k+N} |\mathbb{U}_D(\ell)| = \eta(k) \left( |\mathbb{U}_d| \right)^{\text{floor}(N/L)},$$

from where (11) follows.  $\Box$ 

As a consequence of this result, computation times can be bounded. Furthermore, computation times have an average value, which is proportional to:

$$\frac{1}{L}\sum_{k=1}^{L}N_{\mathbb{U}}(k) = \frac{(|\mathbb{U}_{E}|)^{N}(|\mathbb{U}_{d}|)^{\text{floor}(N/L)}}{L}$$
$$\cdot (L + \text{rem}(N/L)(|U_{d}| - 1)).$$

# 5. STATE SPACE FORMULATION

Given the prediction model (9), at time  $\ell = k$ ,  $V_N$  can be minimized based upon previous decisions  $\tilde{u}'(\ell)$ , where  $\ell < k$ . Alternatives can be devised by formulating (9) in state space.

For that purpose, G(z) is described as:

$$\begin{aligned} x'_{G}(\ell+1) &= A_{G}x'_{G}(\ell) + B_{G}u'(\ell) \\ y'(\ell) &= C_{G}x'_{G}(\ell), \end{aligned}$$
(12)

where  $u'(\ell) = u'_D(\ell) + u'_E(\ell)$ , while  $H_E(z)$  and  $\bar{H}_D(z) \triangleq z^{-\Delta} H_D(z)$  satisfy:

$$\begin{aligned} x'_{D}(\ell+1) &= A_{D}x'_{D}(\ell) + B_{D} \begin{bmatrix} 1 & 0 \end{bmatrix} \widetilde{u}'(\ell) \\ u'_{D}(\ell) &= C_{D}x'_{D}(\ell) \\ x'_{E}(\ell+1) &= A_{E}x'_{E}(\ell) + B_{E} \begin{bmatrix} 0 & 1 \end{bmatrix} \widetilde{u}'(\ell) \\ u'_{E}(\ell) &= C_{E}x'_{E}(\ell) + D_{E} \begin{bmatrix} 0 & 1 \end{bmatrix} \widetilde{u}'(\ell) \end{aligned}$$
(13)

The models (12) and (13) can be combined into an overall prediction model:

$$\begin{aligned} x'(\ell+1) &= Ax'(\ell) + B\widetilde{u}'(\ell) \\ \begin{bmatrix} y'(\ell) \\ u'(\ell) \end{bmatrix} &= Cx'(\ell) + D\widetilde{u}'(\ell), \quad k \le \ell < k+N, \end{aligned}$$
(14)

where:

$$x'(\ell) \triangleq \begin{bmatrix} x'_G(\ell) \\ x'_D(\ell) \\ x'_E(\ell) \end{bmatrix},$$

$$A \triangleq \begin{bmatrix} A_G & B_G C_D & B_G C_E \\ 0 & A_D & 0 \\ 0 & 0 & A_E \end{bmatrix}, B \triangleq \begin{bmatrix} 0 & B_G D_E \\ B_D & 0 \\ 0 & B_E \end{bmatrix}, C \triangleq \begin{bmatrix} C_G & 0 & 0 \\ 0 & C_D & C_E \end{bmatrix}, D \triangleq \begin{bmatrix} 0 & 0 \\ 0 & D_E \end{bmatrix}.$$

The initial state for the prediction model (14) is:

$$x'(k) = x(k) \triangleq \begin{bmatrix} x_G(\ell) \\ x_D(\ell) \\ x_E(\ell) \end{bmatrix}$$

Note that x(k) not only includes plant states, but also channel and source decoder states.

In practical situations, plant and channels are affected by disturbances and transmission errors. Furthermore, plant outputs need to be coded to be sent through the up-link to the controller, see e.g. (Wong and Brocket, 1999; Goodwin *et al.*, 2004). As a consequence, x(k) often needs to be replaced by an observed value  $\hat{x}(k)$ .

To be more precise, in the NCS architecture of Fig. 1, the erasure channel is affected by random transmission errors. Unless a feedback channel is deployed for the down-link which feeds back plant inputs to the controller,  $x_E(\ell)$  needs to be estimated. The simplest method resides in using an open-loop observer (see (13)):

$$\hat{x}_E(k+1) = A_E \hat{x}_E(k) + B_E \widetilde{u}_E(k).$$
(15)

For moderate to high error probabilities, a closed loop observer may be preferable<sup>4</sup>. One way to derive such an observer uses an additive erasure-channel noise model:

$$n_E(\ell) \triangleq -\epsilon(\ell)\widetilde{u}_E(\ell). \tag{16}$$

With this, plant and erasure channel, see (1) and (12), can be re-written as:

$$\begin{bmatrix} x_G(\ell+1) \\ x_E(\ell+1) \end{bmatrix} = \begin{bmatrix} A_G & B_G C_E \\ 0 & A_E \end{bmatrix} \begin{bmatrix} x_G(\ell) \\ x_E(\ell) \end{bmatrix} \\ + \begin{bmatrix} B_G D_E \\ B_E \end{bmatrix} n_E(\ell) + v(\ell),$$
 where:

where

$$v(\ell) \triangleq \begin{bmatrix} B_G D_E \\ B_E \end{bmatrix} \widetilde{u}_E(\ell) + \begin{bmatrix} B_G \\ 0 \end{bmatrix} z^{-\Delta} H_D(z) \widetilde{u}_D(\ell)$$

is a known quantity. A linear filter can be used to estimate  $x_G(\ell)$  and  $x_E(\ell)$  based upon, possibly noisy, plant output measurements, see e.g. (Goodwin and Sin, 1984). It is worth emphasizing here that  $n_E(\ell)$  defined in (16) is, strictly speaking, not an independent noise process. Furthermore,  $A_E$  should have all its eigenvalues strictly inside the unit circle to give good performance, see also (Quevedo *et al.*, 2004*b*).

Note 2. (Implementation). By adapting previous work, see e.g. (Quevedo *et al.*, 2003) it can be shown that the Two-channel Networked Controller can be implemented via linear filters and a vector quantizer, which operate on the reference trajectory and  $\hat{x}(k)$ . Due to space limitations, details are omitted. An interesting point resides in the fact that the constraint set  $\mathbb{U}(\ell)$  is *L*-periodic, see (7). As a consequence, also the vector quantizer, and the networked controller are *L*-periodic.

# 6. EXAMPLE

Consider a model of an NCS aimed at making the level of liquid in a tank follow a reference signal. The system is described by (12) with:

$$A_G = 0.9716, B_G = 0.125, C_G = 0.1979,$$

see (Quevedo *et al.*, 2003). The plant states  $x_G(\ell)$  are here assumed known to the controller.

The down-link is composed of two virtual channels as in Fig. 1. The delay channel has a sub-sampling ratio of L = 4, and a time delay of two time steps  $(\Delta = 2 \text{ in } (2))$ . The data-dropout probability of the erasure channel is  $\mathcal{P} = 10\%$ , see (1). Both channels can only transmit data, which belongs to the ternary sets  $\mathbb{U}_E = \mathbb{U}_d = \{-1, 0, 1\}$ . Following the results of (Quevedo *et al.*, 2004*b*), the source decoders are chosen as:

$$H_D(z) = \frac{0.5z}{z-1}, \quad H_E(z) = \frac{0.5z}{z-0.7}.$$
 (17)

First the situation is examined, where the controller estimates  $x_E(\ell)$  in an open-loop fashion as in (15). Fig. 3 illustrates the results when using the Two-channel Networked Controller with N = 4 and R = 0 in (8). The reference  $r(\ell)$  is chosen as a unitary step signal applied at  $\ell = 10$ .

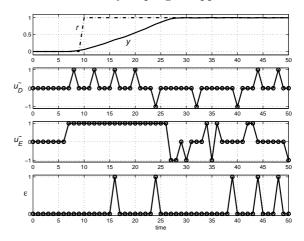


Fig. 3. The two-channel architecture.

For comparison, three other NCSs, which differ from the NCS architecture proposed in the present work by making use of only one of the two available down-link channels, were investigated, see (Quevedo *et al.*, 2003; Quevedo *et al.*, 2004b). The first NCS utilizes the erasure channel and the decoder  $H_E(z)$  given in (17), while the second system deploys the same channel and the integrating decoder  $H_D(z)$ . The third design makes use of the delay channel and  $H_D(z)$ , see (17).

Performance of the above architectures is documented in Fig. 4, where also the results corresponding to the two-channel NCS are included. Note that the NCSs which use  $H_D(z)$  exhibit steady state errors in the erasure channel case and excessive overshoot when operating over the delay channel. On the other hand, using the stable source decoder  $H_E(z)$  avoids these phenomena at the expense of a somewhat sluggish response to reference changes. These observations agree with the design trade-offs elucidated in (Quevedo *et al.*, 2004*b*). All these one-channel schemes are outperformed by the proposed two-channel NCS.

Also in the ideal full-information case where  $x_E(\ell)$  is known to the controller, the two-channel NCS outperforms all one-channel architectures as can be seen in Fig. 5. It is worth noticing that use of the integrating source decoder over the erasure channel now gives zero steady state error.

# 7. CONCLUSIONS

This paper has described an NCS architecture comprising a down-link communication system which can operate in two modes. The scheme

<sup>&</sup>lt;sup>4</sup> Note that  $x_D(k)$  is error-free and an open-loop observer similar to (15) gives exact results.

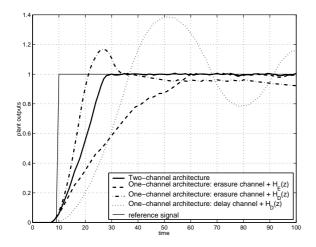
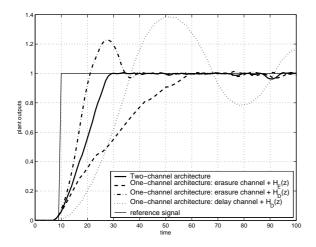
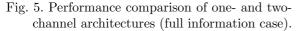


Fig. 4. Performance comparison of one- and twochannel architectures.





equipped with source coders and an appropriate networked controller has been shown to outperform existing one-channel architectures.

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