

SPECIFICATION AND SUPERVISORY CONTROL FOR MULTI-AGENT PRODUCT SYSTEMS

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Abstract: The prescription of sets of trajectories for controlled finite deterministic automaton G is formulated via the notion of the class of specifications denoted, both individually and collectively, by $SPEC$. Next, the formulation and (language) specification of structures for interacting automata are developed within the *Multi-Agent (MA) product* framework (Hubbard and Caines, 1999), and specifications are defined in terms of SPECs. Necessary and sufficient conditions for the synthesis of MA supervisors are given and an associated *MA product of specifications* is introduced; finally, illustrative examples for the results in the paper are provided.

Keywords: trajectory specification, supervisory control, automata, multi-agent systems, vector words, vector processes

1. INTRODUCTION

Systems in the areas of manufacturing, telecommunications, and transportations are often represented by networks of interacting objects, and in many cases specifications for such systems are naturally formulated in terms of transitions between system states. More specifically, such tasks may include visiting an ordered sequence of states (with possible constraints on visiting other system states) regardless of the event sequence by which this is achieved. For example, consider the operation of paying for merchandise in a shop. Regardless of the type of payment (credit card, debit card, check, etc.) it must be completed successfully by an authorization. Such design and control problems arise for the scalar systems represented by finite deterministic automata, as well as for vector (multi-agent) systems. For the latter, we use a formal theoretic framework of *Multi-Agent (MA) product* systems introduced in Hubbard and Caines (1999). Further development of the ideas for the analysis, con-

trol, and optimization of such systems is to be found in Romanovski and Caines (2001a,b). The results of this paper constitute a natural extension of the classical supervisory control results (see Kumar and Garg, (1995), among others) for scalar systems to the more general MA product system case. Illustrative examples for the results in the paper are provided in Sections 3 and 4.

2. SCALAR SPECIFICATIONS FOR FINITE AUTOMATA

Definition 1. A *specification (SPEC)* for a given automaton G is a 4-tuple of subsets of X , namely, $SPEC = \{X_I, X_T, X_{pc}, X_{bad}\}$, where $X_{pc} \cap X_{bad} = \emptyset$. X_I is termed the set of initial states (of the $SPEC$), $X_T \subseteq X_m$ is termed the set of terminal states, X_{pc} is an *ordered* subset of X (possibly with repetitions) termed the set of ports of call and X_{bad} is termed the set of bad states. \square

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X_{pc} is the set of states which should be visited in order while X_{bad} is the set of states which must be avoided. Further, unless otherwise stated, X_I and X_T are singletons ($\{x_I\}$ and $\{x_T\}$ respectively).

The term *to drive a state x (of an automaton) to state y* means that there exists an input word of controllable and uncontrollable events a such that when the automaton is in the state x and accepts the word a the automaton terminates in state y , equivalently, y is reachable from x via an input sequence $a \in \Sigma^*$.

Definition 2. We say that an automaton $G = (X, \Sigma, \delta, x_o, X_m)$ satisfies the *SPEC* $= \{x_I, x_T, X_{pc}, X_{bad}\}$ if there exists a system trajectory t which satisfies:

- (1) The initial automaton state x_o is driven along t to the state x_I without entering the set X_{pc} .
- (2) t contains all the elements of X_{pc} in the given order.
- (3) The trajectory t from x_o to the x_T does not meet the set of potentially bad states,

where a potentially bad state is a state in X_{bad} , or a state from which a bad state is reachable by a sequence of uncontrollable events. \square

The solution to the problem of satisfying a specification *SPEC* for a given automaton can be divided into two steps: (a) eliminate all potentially bad states X_{pbad} (b) within the resulting set, $X - X_{pbad}$, establish the existence of a trajectory that visits all elements in X_{pc} in the order given by *SPEC*. Both steps were discussed and solutions were developed in Romanovski and Caines (2001a,b).

To simplify the notation, we include the initial and terminal state into X_{pc} as the first and last element respectively and represent a *SPEC* as a pair $\langle X_{pc}, X_{bad} \rangle$. Denote by $L_m(SPEC)_G$ the set of all trajectories that satisfy the above definition (a formal definition for $L_m(SPEC)_G$ can be found in Romanovski and Caines, (2001b)). Evidently, $L_m(SPEC)_G$ is often not prefix closed. In fact, we have the following

Proposition 3. Let G be finite automaton for which $X_m = X$, and let $\langle X_{pc}, X_{bad} \rangle$ be a *SPEC* for G , $L_m(SPEC)_G \neq \emptyset$. If $|X_{pc}| > 2$ then $L_m(SPEC)_G$ is not prefix closed. If $X_{pc} = \emptyset$ (i.e. the specification has only bad states), then $L_m(SPEC)_G$ is prefix closed. \square

Definition 4. An automaton $G \downarrow_{SPEC} = (Y, \Sigma, \delta_1, Y_o, Y_m)$ is called a *restriction of G according to *SPEC** if (i) $Y = X - X_{pbad}$, (ii) $\delta_1 = \delta \downarrow_Y$, where \downarrow denotes the restriction operation of the domain of a partial function to the indicated set, and (iii) $Y_o = X_o - X_{pbad}$, $Y_m = X_m - X_{pbad}$. \square

Proposition 5. Let G be finite automaton, $X_m = X$, and let $\langle \emptyset, X_{bad} \rangle$ be a *SPEC* for G . $L_m(SPEC)_G$ is controllable w.r.t. G if and only if $X_{bad} = X_{pbad}$.

Proof. Let $L_m(SPEC)_G$ be controllable, and let $\delta^*(x_o, a) = x \notin X_{bad}$. Then for $a \in L_m(SPEC)_G$ and for any uncontrollable event u defined at state x we have that $au \in L_m(SPEC)_G$ and hence $\delta(x, u) \notin X_{bad}$. Thus, there is no uncontrollable event that leads from $x \notin X_{bad}$ to a state from X_{bad} . By definition $X_{bad} = X_{pbad}$.

Let $X_{bad} = X_{pbad}$. Then whenever $x \notin X_{bad}$ and an uncontrollable u is defined at x , $\delta(x, u) \notin X_{bad}$, or, in other words, for any $a \in L_m(SPEC)_G$ and $au \in L(G)$ we have that $au \in L_m(SPEC)_G$. \square

Corollary 6. Let G be finite automaton, for which $X_m = X$. Let $\langle \emptyset, X_{bad} \rangle$ be a *SPEC* for G , and assume $L_m(SPEC)_G \neq \emptyset$. There is Σ_U -enabling supervisor for $L_m(SPEC)_G$ if and only if $X_{bad} = X_{pbad}$. \square

Corollary 7. In the setup of the previous proposition, $L(G) \downarrow_{SPEC}$ is the maximal controllable sublanguage w.r.t. G . \square

3. SUPERVISION OF MA SYSTEMS

The standard interaction for the supervisor-system pair is that of the synchronous product (see Kumar and Garg, (1995), for example). An automaton $S = (Y, \Sigma, \delta_S, y_0, Y_m)$ representing the supervisor operates with the plant $G = (X, \Sigma = \Sigma_c \cup \Sigma_u, \delta, x_0, X_m)$, and the resulting language is the scalar synchronous product $L(S) \parallel_s L(G)$ (see Kumar and Garg, 1995).

An alternative is to consider control of a system G with a supervisor S acting in unison, as an individual agent, leading to the combined evolution $L(S) \parallel_{MA} L(G)$. In what follows it is assumed that all languages are prefix-closed, hence both the terms $L(S \parallel_{MA} G)$ and $L(S) \parallel_{MA} L(G)$ can be used equivalently (note the latter is only defined for prefix closed languages). This assumption extends to specification languages (e.g. K below). It is also assumed that the goal states X_m and Y_m are the entire states X and Y . This has the effect of simplifying the algebraic derivations by alleviating the need for a *non-marking* condition for the supervisor (as in Kumar and Garg, 1995) and isolating the controllability criteria.

The results regarding controllability of a language and the synthesis of synchronous product based supervisors apply almost directly for scalar specifications K when the MA product is used in lieu of the scalar synchronous product. For vector specifications, however, controllability is not enough for the synthesis of MA-supervisors. This is due to the fact that in MA-product

we often cannot disable an isolated (disableable) event, but only the controllable components of a given event.

Let the plant model consists of the MA product of automata and let the components be

$$G_i = (X_i, \Sigma_{i_c} \cup \Sigma_{i_u}, \delta_i, x_{0_i}, X_{m_i}), i = 1, 2, \dots, N$$

where Σ_{i_c} are the disable events and Σ_{i_u} are the undisable events. It is assumed that (for the case $N = 2$),

$$\Sigma_{i_c} \cap \Sigma_{j_u} = \emptyset, \Sigma_{i_u} \cap \Sigma_{j_c} = \emptyset,$$

which forces the events of the form $[a \dots a]^T$ to be uncontrollable or controllable in both components. The Definitions 8 and 9 below are written for $N = 2$, but easily generalised for an arbitrary N .

Definition 8. Multi-Agent Product (Supervisory Case)

$$\begin{aligned} G_1 ||_{MA} G_2 &= \\ &= (X_1 \times X_2, \Sigma_C \cup \Sigma_U, \delta_{MA}, (x_{0_1}, x_{0_2}), X_{m_1} \times X_{m_2}) \end{aligned}$$

where,

$$\Sigma_U = \left\{ \begin{bmatrix} a \\ b \end{bmatrix} \mid a \in \Sigma_{1_u} \text{ and } b \in \Sigma_{2_u} \right\} \quad (1)$$

$$\Sigma_C = \left\{ \begin{bmatrix} a \\ b \end{bmatrix} \mid a \in \Sigma_{1_c} \text{ or } b \in \Sigma_{2_c} \right\} \quad (2)$$

$$\delta_{MA} \left(\begin{bmatrix} x \\ y \end{bmatrix}, \begin{bmatrix} a \\ b \end{bmatrix} \right) = \begin{bmatrix} \delta_1(x, a) \\ \delta_2(y, b) \end{bmatrix}, \text{ if}$$

$$\delta_1(x, a)! \wedge \delta_2(y, b)! \wedge (a = b \vee (\neg \delta_2(y, a)! \wedge \neg \delta_1(x, b)!)),$$

and undefined otherwise. The notation $\delta_1(x, a)!$ means that δ is defined at (x, a) . \square

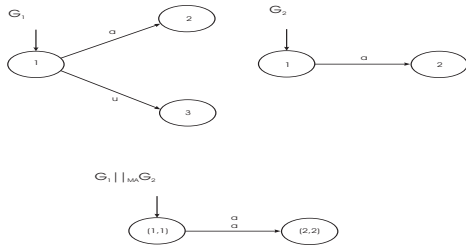


Fig.1: Mechanical disabling

Other constructions of product systems can be found in Kam at al., (1997), Hartmanis and Stearns, (1966) and Li and Wonham, (1993). It is easy to see that it is possible that some uncontrollable events defined for the automaton G_1 (or G_2) can be prevented by *synchronization*, as is shown on Fig.1. Moreover, even though the specification $K = \{a\} \subset L(G_1) = \{a, b\}$ is not controllable (we assume that b is uncontrollable), we have that $L(G ||_{MA} S) = \left\{ \begin{bmatrix} a \\ a \end{bmatrix} \right\}$, since $\begin{bmatrix} b \\ a \end{bmatrix}$ cannot occur by the construction of the MA product.

In order to eliminate the prevention of uncontrollable event mechanically (i.e. by construction of the MA-product), we need to introduce a Σ_U -enabling MA-product. For the construction of supervisor S, it can be

considered as an MA-analogy of scalar Σ_U -enabling (Kumar and Garg, 1995).

Assume that the MA-product of G_1 and G_2 , namely, $G_1 ||_{MA} G_2 = (Z = Y \times X, \Sigma = \Sigma_C \cup \Sigma_U, \delta_{MA}, z_0 = (y_0, x_0), Y_m \times X_m)$ is defined, and δ_{MA}^* is a natural extension of δ_{MA} on Σ^* , which is defined as

$$\left[\begin{array}{c} \Sigma_{G_1} \\ \Sigma_{G_2} \end{array} \right]^* = \left\{ \begin{bmatrix} v \\ w \end{bmatrix} \mid v \in \Sigma_{G_1}^*, w \in \Sigma_{G_2}^*, |v| = |w| \right\}.$$

Definition 9. For any vector state $z \in Z$,

$$\mathcal{P}_{G_1, z}^{-1}(a) \stackrel{\text{def}}{=} \left\{ \begin{bmatrix} a \\ b \end{bmatrix} \in \Sigma \mid \delta_{MA}(z, \begin{bmatrix} a \\ b \end{bmatrix})! \right\}, a \in \Sigma_1.$$

Similarly,

$$\mathcal{P}_{G_2, z}^{-1}(d) \stackrel{\text{def}}{=} \left\{ \begin{bmatrix} c \\ d \end{bmatrix} \in \Sigma \mid \delta_{MA}(z, \begin{bmatrix} c \\ d \end{bmatrix})! \right\}, d \in \Sigma_2.$$

where $\Sigma_i = \Sigma_{U_{G_i}} \cup \Sigma_{C_{G_i}}$ defined for automata G_i , $i = 1, 2$ \square

We generalise the notion of *component-wise projection* (see Hubbard and Caines, (1999), Romanovski and Caines (2001a))

$\mathcal{P}_i : (\Sigma_1 \times \dots \times \Sigma_N)^* \rightarrow \Sigma_i^*$ to \mathcal{P}_{G_i} as follows:

Definition 10. Let the MA product $G_1 ||_{MA} G_2$ be defined. For any vector word

$$s = [a_1, \dots, a_N, a_{N+1}, \dots, a_M]^T \in L(G_1 ||_{MA} G_2), [a_1, \dots, a_N]^T \in L(G_1), [a_{N+1}, \dots, a_M]^T \in L(G_2),$$

$$\mathcal{P}_{G_1}(s) \stackrel{\text{def}}{=} [a_1, \dots, a_N]^T,$$

and \mathcal{P}_{G_2} is defined similarly. \square

Definition 11. Σ_U -enabling MA-product.

$G_1 ||_{MA} G_2$ is Σ_U -enabling if the following condition holds:

whenever

$$z \in Z, s \in \Sigma^*, w_i \in \Sigma_{U_{G_i}}, i = 1, 2$$

are such that $\delta_{MA}^*(z_0, s) = z$ and $\mathcal{P}_{G_i}(s)w_i \in L(G_i)$, then it is the case that

$$\mathcal{P}_{G_i, z}^{-1}(w_i) \neq \emptyset.$$

\square

Note that the condition of the above definition implies $\emptyset \neq s\mathcal{P}_{G_i, z}^{-1}(w_i) \subseteq L(G_1 ||_{MA} L(G_2))$. In other words, if the vector state z is reachable from the initial state z_0 of $G_1 ||_{MA} G_2$ and some uncontrollable (vector or scalar) event $w_1 \in \Sigma_{U_{G_1}}$ (or $w_2 \in \Sigma_{U_{G_2}}$) is defined for a component of z that belongs to G_1 (or, respectively, to G_2), then there must be some event $v_2 \in \Sigma_{G_2}$ (respectively, $v_1 \in \Sigma_{G_1}$) such that $\delta_{MA}(z, [w_1, v_2]^T)$ is defined (respectively, $\delta_{MA}(z, [v_1, w_2]^T)$ is defined).

Definition 12. An MA supervisor S for G is called MA Σ_U -enabling if and only if $S ||_{MA} G$ is a Σ_U -enabling MA product. \square

Lemma 13. Let K and L be prefix-closed languages. Then,

$$K \subseteq L \implies \implies K \parallel_{MA} L = K \parallel_{MA} K = \left\{ \begin{bmatrix} a \\ a \end{bmatrix} \mid a \in K \right\}.$$

□

For an MA supervisor we assume $\Sigma_S \subseteq \Sigma_G$.

Definition 14. An MA supervisor S for G is called *MA Σ_U -enabling* if and only if $S \parallel_{MA} G$ is a Σ_U -enabling MA product. □

Definition 15. Let G be an MA-product system and K be a prefix-closed vector specification. K is called *MA controllable* (w.r.t. G) if and only if the the following conditions are true:

- (1) K is controllable (i.e. $\overline{K} \Sigma_U \cap L(G) \subseteq \overline{K}$);
- (2) $\forall a \in \Sigma_G, s \in K, x \in X \{ \delta_G^*(x_0, s) = x \wedge sa \in L(G) \wedge sa \notin K \} \implies \exists a_i \in \Sigma_{i_c}, i = 1, \dots, N$ such that $([sP_{G_i,x}^{-1}(a_i)] \cap K = \emptyset)$ □

We paraphrase the Condition 2 of the above definition as follows: if a controllable vector event a defined at a given vector state x , takes us out of the specification K , there must be some controllable component of a , say a_i , such that any vector event that is defined at x and has a_i as a component, takes us out of K . Note that if K is a scalar specification, the Condition 2 is trivially true.

Theorem 1. Let $K \subseteq L(G)$ be a regular (i.e. finitely generated) vector specification for an MA product system G .

- (1) K admits a Σ_U -enabling MA supervisor S such that $P_G(L(S) \parallel_{MA} L(G)) = K$ if and only if K is MA controllable.
- (2) If K is not MA controllable, then there exists a maximal (w.r.t. the inclusion partial order) specification $K_1 \subset K$ which is MA controllable w.r.t. G .

Proof.

Part 1. We construct an $S = (X_S, \Sigma = \Sigma_C \cup \Sigma_U, \delta^S, x_0)$ by the following rules:

- (1) $X_S = \{[s](R_K) \mid s \in K\}$, where R_K is an equivalence relation induced by K according to the Myhill-Nerode construction (see Kumar and Garg (1995)). Since K is regular, R_K is of finite index.
- (2) For any state $x \in X_S$ any vector event $a \in \Sigma$,

$$\delta^S(x, a) = [sa](R_K)$$

if and only if $sa \in K$. For each vector event a such that $sa \notin K$ we disable (i.e. do not define) any controllable a_i at each component x_i of $x \in X_S$ for which

$$sP_{G_i,x}^{-1}(a_i) \cap K = \emptyset$$

Other words, we make $L(S) = K$. Since K is controllable, $S \parallel_{MA} G$ is Σ_U -enabling MA product, so S is Σ_U -enabling. By Lemma 12 we have that $P_G(L(S) \parallel_{MA} L(G)) = K$.

Assume that such S exists. Then K is controllable since $S \parallel_{MA} G$ is Σ_U -enabling MA product. Assume there exist a vector state $x \in X$, vector word $s \in K$, and an event $a \in \Sigma_G$ for which the Condition 2 of the theorem is not true. We cannot leave this event in S since then $L(S) \neq K$. On the other hand, by the disabling of any component of vector event a we disable some event that belongs to K since for any $i = 1, \dots, N$

$$sP_{G_i,x}^{-1}(a_i) \cap K \neq \emptyset$$

But, again, $L(S) \neq K$ and so $P_G(L(S) \parallel_{MA} L(G)) = L(S) \neq K$. Contradiction.

Part 2. Consider two cases:

Case 1. K is controllable but not MA controllable. In this case the algorithm for finding a K_1 is the following: we start with the initial state x_0 . If all vector events defined at this state satisfy the Condition 2 of the definition of MA controllability, we move to all states that are directly accessible (i.e. by one transition) from x_0 . Suppose at state x condition 2 is violated. We remove events from K as follows: For each $s \in K, a \in \Sigma$ such that $\delta^*(x_0, s) = x$, and $sa \notin K$, we find a component a_i such that the cardinality of the set $sP_{G_i,x}^{-1}(a_i) \cap K$ is minimal and remove from K all elements of the type $sP_{G_i,x}^{-1}(a_i)$; then move to the states which are still accessible by the elements in the reduced language K' . Continue the procedure until all states accessible from x_0 satisfy Condition 2. Note that since K is controllable, the resulting set K_1 will also be controllable and satisfy the condition 2 by construction, so K_1 will be MA controllable.

Case 2. Let K be uncontrollable w.r.t. G . There is a procedure (see Kumar and Garg (1995), among others) for finding the maximal controllable sublanguage of K , denoted by K'_1 . As it is shown in Romanovski and Caines (2001a), K'_1 , in general, does not satisfy the Condition 2. In order to obtain K_1 , we apply to K'_1 the algorithm described above. □

Lemma 16. Let the specifications K_1 and K_2 be controllable w.r.t. the automata G_1 and G_2 respectively. Then $K_1 \parallel_{MA} K_2$ is controllable w.r.t. $G_1 \parallel_{MA} G_2$.

Proof. Let $s = [s_1, s_2]^T \in L(K_1 \parallel_{MA} K_2) \cap L(G_1 \parallel_{MA} G_2)$, $[u, v]^T \in \Sigma_U$, and $[s_1, s_2]^T [u, v]^T \in L(G_1 \parallel_{MA} G_2)$. Then $s_1 u \in L(G_1)$ and, since K_1 is controllable w.r.t. G_1 , $s_1 u \in K_1$. Similarly, $s_2 u \in K_2$. As a result, $[s_1, s_2]^T [u, v]^T = [s_1 u, s_2 v]^T \in K_1 \parallel_{MA} K_2$. □

Proposition 17. Let the specifications K_1 and K_2 be MA controllable w.r.t. the automata G_1 and G_2 respec-

tively. Then $K = K_1 \parallel_{MA} K_2$ is MA controllable w.r.t. $G = G_1 \parallel_{MA} G_2$.

Proof. Due to the above lemma, it is enough to prove that the Condition 2 of the definition of MA controllability is true. Assume that for some vector state x of MA product $G_1 \parallel_{MA} G_2$, some vector word s such that $\delta_{MA}^*(x_0, s) = x$ and $s \in K_1 \parallel_{MA} K_2$, and some controllable vector event a defined at x we have that $sa \in L(G_1 \parallel_{MA} G_2)$, but $sa \notin K_1 \parallel_{MA} K_2$. Denote $x = [x_1, x_2]^T$, where $x_1 \in X_1, x_2 \in X_2, P_{G_1}(sa) = s_1 a_1, P_{G_2}(sa) = s_2 a_2$. Then either $s_1 a_1 \notin K_1$ or $s_2 a_2 \notin K_2$, or both. Since K_1 is MA controllable w.r.t. G_1 , we have that there exists a controllable component a_{1i} of a_1 such that $s_1 P_{G_1, x_1}^{-1}(a_{1i}) \cap K_1 = \emptyset$. But that implies $s P_{G, x}^{-1}(a_{1i}) \cap K = \emptyset$. In the second case (as well as in the third) we get the same result by MA controllability of K_2 . Thus, $K = K_1 \parallel_{MA} K_2$ is MA controllable w.r.t. $G = G_1 \parallel_{MA} G_2$. \square

Let K_1 be the specification for G_1, K_2 be the specification for G_2 . Naturally, we assume that $K_1 \subseteq L(G_1), K_2 \subseteq L(G_2)$, and we consider the MA product $K_1 \parallel_{MA} K_2$ w.r.t. $G_1 \parallel_{MA} G_2$, which is formally defined as follows:

Definition 18. The MA specification $K_1 \parallel_{MA} K_2$ for the MA product $G_1 \parallel_{MA} G_2$ is given by $(K_1 \parallel_{MA} K_2) \cap L(G_1 \parallel_{MA} G_2)$. \square

Proposition 19. Let the specifications K_1 and K_2 be MA controllable w.r.t. the automata G_1 and G_2 respectively by the MA supervisors S_1 and S_2 respectively. If $G_1 \parallel_{MA} G_2$ is a Σ_U -enabling MA product, then the MA specification $K_1 \parallel_{MA} K_2$ has an MA supervisor $S_1 \parallel_{MA} S_2$ w.r.t. $G_1 \parallel_{MA} G_2$.

Proof. Denote $G = G_1 \parallel_{MA} G_2, K = K_1 \parallel_{MA} K_2, S = S_1 \parallel_{MA} S_2$. We have that $L(S) \subseteq L(G)$ and since G is Σ_U -enabling, $S \parallel_{MA} G$ is Σ_U -enabling. Moreover, we have that $P_{G_1}(S \parallel_{MA} G) = K_1, P_{G_2}(S \parallel_{MA} G) = K_2$, that gives $P_G(S \parallel_{MA} G) = K$.

Example 1. Consider the vector specification $K_1 = \{[a, c]^T, [b, d]^T\}$ for $G_1 \parallel_{MA} G_2$, given on Fig.2. K_1 fails to satisfy the condition 2 of the theorem, and it is impossible to disable any component without losing some element of K . Consider the additional specification for $G_2, K_2 = \{c\}$. Then vector specification $K_1 \parallel_{MA} K_2 = \{[a, c, c]^T\}$ is MA controllable w.r.t. $G_1 \parallel_{MA} G_2 \parallel_{MA} G_2$. \square

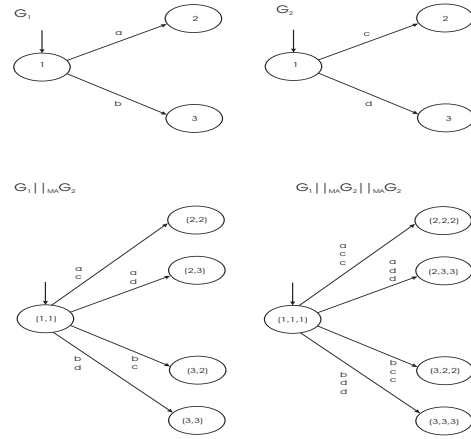


Fig.2: Examples of MA Products

4. VECTOR SPECS FOR MA PRODUCT SYSTEMS

There are two ways to analyse the specifications for MA product systems. Since the MA product is a finite deterministic automaton, we can construct specifications directly for such automaton and directly apply the results from Romanovski and Caines (2001b). The other approach is to give a specification for an MA system via specifications for its component systems.

Example 2. Consider the complex system of interaction of customer and sales department in the small shop. It is clear that whenever the customer has successfully paid for the item, we must remove it from the shelf. The specification is to keep shelves non-empty when a customer is in the shop.

The behaviour of the Customer (G_1) is represented in Fig.3. Here, the events *out*, *full stock* (i.e. the demand of a customer to show all items available), *activate*, *complete*, *incomplete* are not controllable, and the other events, *stay*, *try again* and *refuse* are controllable.

The scheme for a “shop” includes two automata: “Counter” (G_2) which represents a mechanism for paying and “Shelf” (G_3), which carries information about items available. General automata for “Counter” and “Shelf” are given in Fig.3 also. We denote by $WAIT_{G_2}$ the set of events $\{stay, out, full stock\}$ defined at the state *Idle* of the automata G_2 . The events *complete_i*, ($i = 1 \dots 3$), *full stock_j*, ($j = 1, 2$ and *refill* are controllable.

To be specific, we assume here that the capacity of the shelf is 3 items, and allow 3 attempts to complete the procedure of paying before the final refusal. It is clear that a similar automaton can be constructed for an arbitrary number of attempts and an arbitrary shelf capacity.

We represent the behaviour of the whole system as the MA product of G_1, G_2 , and G_3 , denoted by G .

