## Synthesis and OFET Performance of Functionalized Pentacene Derivatives

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## Introduction

Organic thin-film field-effect transistors (OFETs) are of great interest to researchers because of their potential to offer light-weight, flexible, low-cost alternatives to devices currently utilizing the traditional silicon-based transistor. The semiconducting organic materials can often be solution processed, which would allow for deposition via inkjet or reel-to-reel printing onto flexible, plastic substrates.<sup>1</sup> Further, the synthesis of organic materials is versatile in that the molecules can be functionalized to promote desired changes in solubility, stability, and electronic properties.

Pentacene was among the first molecules to show high field-effect performance.<sup>2</sup> Although pentacene has demonstrated great promise in this field, its derivatives have yet to be widely studied. A few groups have investigated the functionalization of pentacene at the central and terminal rings. Wudl, Bao, and coworkers<sup>3</sup> substituted the hydrogen atoms in the 2, 3, 9, and 10 positions with methyl groups. Anthony et al.<sup>4</sup> and Neckers et al.<sup>5</sup> reported the synthesis of 6,13-substituted pentacene derivatives with either electron-withdrawing or trialkylsilylethynyl groups substituted on the terminal rings. Functionalization of the pentacene core has the potential to improve electronic properties by adjusting the HOMO and LUMO levels of the molecule, altering the size and shape of the molecule and thus changing the packing mode, and possibly improving stacking.

We report the synthesis of four new functionalized pentacene derivatives based on symmetrical and asymmetrical substitutions, as illustrated in Scheme 1. Thin film transistors have been fabricated for two of the molecules, and their device performance is also reported.

## **Results and Discussion**

Synthesis of the pentacene derivatives was performed in a multi-step reaction scheme as shown in Scheme 1.



Scheme 1: Synthetic Scheme

The tetrabrominated starting materials were synthesized in moderate yields using a method similar to literature procedures.<sup>6</sup> These compounds underwent a Diels-Alder cyclization reaction to produce the substituted pentacene precursors. In a procedure similar to what is reported in literature,<sup>7,8</sup> the reaction of the tetrabrominated compounds with 1,4-anthraquinone in the presence of NaI and anhydrous DMF at 60°C afforded asymmetric quinones in 60-88% yields while reaction with *p*-benzoquinone under the same conditions gave symmetric quinones in slightly lower yields. The quinones were then converted to the corresponding pentacene derivatives in 38-71% yields by reduction in the presence of aluminum wire, cyclohexanol, and catalytic amounts of HgCl<sub>2</sub> and CBr<sub>4</sub>. The crude products were purified via sublimation prior to device fabrication.

In order to determine the stability of the molecules, thermal gravimetric analysis (TGA) was performed for pentacene derivatives under a stream of nitrogen gas at a heating rate of 10°C/min. The decomposition temperature was defined as the temperature at which a 5 % weight loss takes place. We found that the decomposition temperature of 2,3-dibromopentacene ( $Br_2P$ ) was 15 °C higher than that of 2,3,9,10-tetrabromopentacene ( $Br_4P$ ), even though  $Br_2P$  has a smaller molecular weight. As a result,  $Br_2P$  could be purified by multi-sublimation to give enough pure compounds for the measurement of device performance; however, it was difficult to obtain pure  $Br_4P$ .

Top-contact thin-film transistors were fabricated on octadecyltrimethoxysilane (OTS) treated SiO<sub>2</sub>/Si substrates. **Br**<sub>2</sub>**P** and 2-cyanopentacene (**CNP**) were thermally evaporated at different substrate temperatures. All devices operated as *p*-type transistors. The field-effect mobility was calculated using the  $I_{DS}$  values in the saturation regions, as summarized in Table

1. **Br**<sub>2</sub>**P** devices yielded a mobility of 0.21 cm<sup>2</sup>V<sup>-1</sup>s<sup>-1</sup> at substrate temperatures ranging from 80-90°C, while **CNP** devices exhibited mobility near  $10^{-2}$  cm<sup>2</sup>V<sup>-1</sup>s<sup>-1</sup>. The field-effect mobility of **Br**<sub>2</sub>**P** increased with increasing  $T_D$ , which is attributed to better ordered thin films and larger grain sizes at elevated  $T_D$ . As shown in Figure 1, the AFM images also showed sharper grain boundaries and larger crystal size for higher  $T_D$  thin films. Thus, the grain sizes for **Br**<sub>2</sub>**P** was on the order of 250-400 nm at  $T_D = 25$  °C, while its sizes became bigger at 90 °C, on the order of 500-750 nm. Ongoing research of the synthesis and device performance of other pentacene derivatives is currently being conducted.

| Compound          | Surface<br>Treatment | Deposited<br>Temperature<br>(°C) | Mobility<br>$\mu (cm^2 V^{-1} s^{-1})$ | On/Off ratio<br>I <sub>on</sub> /I <sub>off</sub> |
|-------------------|----------------------|----------------------------------|--|---|
| Br <sub>2</sub> P | OTS                  | 25                               | 0.11                                   | $2.8 \times 10^5$                                 |
|                   |                      | 70                               | 0.17                                   | $1.2 \times 10^5$                                 |
|                   |                      | 80                               | 0.21                                   | $2.2 \times 10^5$                                 |
| CNP               | OTS                  | 25                               | 0.0019                                 | $2.9 \times 10^2$                                 |
|                   |                      | 70                               | 0.0087                                 | $5.6 \ge 10^2$                                    |
|                   |                      | 80                               | 0.0092                                 | $6.5 \times 10^2$                                 |

**Table 1.** Field-effect mobilities of transistors prepared at different substrate temperature ( $T_D$ ) using top-contact geometry.



**Figure 1 [Upper].** AFM (phase) of **Br**<sub>2</sub>**P** deposited at a) 25 °C b) 70 °C and c) 90 °C. [**Bottom]** a)  $I_{DS}$  vs  $V_{DS}$  characteristic of **Br**<sub>2</sub>**P** (90 °C) at different gate voltages prepared at elevated substrate temperature with channel length L = 200 µm and channel width W = 4000 µm, b)  $I_{DS}$  vs  $V_{G}$  characteristics of **Br**<sub>2</sub>**P** thin film transistors.

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