

Distributed Control Design with Robustness to Small Time Delays

R. S. Chandra
 Mechanical & Aerospace Engineering
 Cornell University.
 rsc28@cornell.edu

C. Langbort
 Center for the Mathematics of Information
 California Institute of Technology
 clangbort@ist.caltech.edu

R. D'Andrea
 Mechanical & Aerospace Engineering
 Cornell University
 rd28@cornell.edu

Abstract—Recent results by the authors have shown how to construct a class of structured controllers for large scale spatially interconnected systems via linear matrix inequalities. These controllers guarantee that the closed loop interconnected system is well-posed, stable and has \mathcal{H}_∞ norm less than unity. Of paramount importance in the control of interconnected systems is the requirement that the stability and performance of the controlled system be robust to arbitrarily small communication delays between subsystems; this amounts to a continuity property. In this paper, it is shown how to realize the structured controllers obtained from the linear matrix inequalities in order to ensure this continuity property for the closed loop system.

I. INTRODUCTION

In recent years, considerable research attention has been focused on the theory and practice of large scale spatially interconnected systems. The papers [2] and [5] proposed control design techniques for a class of interconnected systems known as spatially invariant systems; the work [10] dealt with analysis of a class of spatially interconnected systems called the multidimensional (MD) systems. The main motivation for these results is the fact that it is often not practicable to design fully centralized controllers for large scale interconnected systems due to constraints on the computational cost and allowable interconnection topology.

In recent papers [12], [11], based in turn on the results of [7] and [16], the authors have extended the results of [5] and have proposed a method to construct a class of structured controllers for spatially interconnected systems. Briefly, these results can be summarized as follows: Consider a linear time-invariant (LTI) plant consisting of ' L ' subsystems G_i , $i = 1, \dots, L$, which are interconnected over an arbitrary graph. Every node of the graph represents an LTI system and every directed edge represents the signals flowing from one node to the other. Each edge of the graph connecting subsystems G_i and G_j , $i \neq j$ is associated with the pair of numbers m_{ij}^+, m_{ij}^- , where m_{ij}^+ denotes the dimension of the output of G_i flowing towards G_j and where m_{ij}^- denotes the dimension of the output of G_j flowing towards G_i . In addition, each subsystem is affected by the exogenous disturbance d_i and outputs the signal z_i . Notice that $m_{ij}^+ = m_{ji}^-$ for all $i, j = 1, \dots, L$.

Suppose we seek a controller consisting of subsystems K_i with the properties that:

- 1) Each K_i is connected to G_i .
- 2) Two controller subsystems K_i, K_j can communicate; that is, K_i can output a signal of dimension $m_{ij}^{K_i,+}$ to K_j , and receive an input of dimension $m_{ij}^{K_i,-}$ from K_j , so that $m_{ij}^{K_i,+} = m_{ji}^{K_i,-}$ for all $i, j = 1, \dots, L$.
- 3) The inequality $\max(m_{ij}^{K_i,+}, m_{ij}^{K_i,-}) \leq \alpha \max(m_{ij}^+, m_{ij}^-)$ holds, where α is an absolute constant.
- 4) The closed loop system is stable and \mathcal{H}_∞ norm of the system mapping (d_1, \dots, d_L) to (z_1, \dots, z_L) is less than unity.

Notice that the third condition above imposes a restriction on the possible connectivity of the controller subsystems in terms of that of the plant; in particular, no two controller subsystems can communicate unless the corresponding plant subsystems do. It was shown in [12], [11] that the above problem can be solved with a bound $\alpha = 3$, and a linear time-invariant control system explicitly computed, if a certain linear matrix inequality (LMI) condition is satisfied.

The very fact that we are trying to control a plant consisting of spatially distributed subsystems requires that we add a fifth

condition to the four already given above, namely, that the closed loop system be stable, and the \mathcal{H}_∞ norm of the system be less than unity, when arbitrarily small communication delays are present between any two closed-loop subsystems. It has been recognized long ago (see [18]) that seemingly innocent examples of LTI feedback systems have the property that arbitrarily small delays in the feedback loop can destroy stability. For example, a constant gain $\hat{P}(s) = q > 1$ (s denotes the Laplace transform variable) in feedback with a unity gain has this property; if $\hat{P}(s)$ is replaced by $qe^{-\epsilon s}$ (a time delay of $\epsilon > 0$ seconds), the resulting system is no longer internally stable. In the paper [13], it is shown that for an LTI feedback system, robustness with respect to small delays in the feedback loop is equivalent to the stability of the nominal system and the condition that a certain matrix structured singular value be less than unity. Thus, it is not *a priori* obvious that the closed loop system is robustly stable when small communication delays are present. The aim of this paper is to show the following: if a controller K (with subsystems K_i) is synthesized using the LMI conditions of [11], there exists another controller \bar{K} (with subsystems \bar{K}_i) which can be explicitly computed in terms of the state-space matrices of K_i , has the same structure as K (this is of course essential, since the main contribution of [11] was to construct controllers with this structure), and guarantees robust stability and \mathcal{H}_∞ performance of the closed loop system against arbitrarily small communication delays. *Note that if we directly implement the controller obtained via solving the LMIs of [11], the closed loop may not have this desirable property.*

The paper is organized as follows: In Section II, some background material on spatially interconnected systems as reported in the work [11] is given, and the problem of robustness to arbitrarily small delays is precisely formulated. In Section III, the main results of the paper are presented; a controller is constructed that achieves robust stability and performance to small time communication delays. Section IV presents the conclusions, and the proofs are given in the Appendix.

Notation: The set of the real numbers is denoted by \mathbb{R} , the complex numbers by \mathbb{C} , the real n -vectors by \mathbb{R}^n , the $m \times n$ real (complex) matrices by $\mathbb{R}^{m \times n}$ ($\mathbb{C}^{m \times n}$), the $n \times n$ identity by I_n , and the real symmetric matrices by \mathbb{R}_s^n . M^* is the conjugate transpose of $M \in \mathbb{C}^{m \times n}$. For $M \in \mathbb{R}_s^n$, $M \succ 0$ means M is positive definite; $M \prec 0$ means M is negative definite. The maximum singular value of a matrix M is denoted by $\bar{\sigma}(M)$ and the spectral radius is denoted by $\rho(M)$ if M is square. The Euclidean norm of a vector is denoted by $\|\bullet\|$. \mathcal{L}_2 is the space of vector signals $z(t)$ such that $\int_0^\infty |z(t)|^2 dt < \infty$ and \mathcal{L}_{2e} is the space of signals $z(t)$ such that $\int_0^T |z(t)|^2 dt < \infty$ for all $T > 0$. The \mathcal{L}_2 norm of a signal is denoted by $\|\bullet\|$ and the \mathcal{H}_∞ norm of an LTI system G is denoted by $\|G\|_\infty$; for more details, see [6]. Given matrices M_k , $k = 1, \dots, n$, the notation $\mathbf{diag}_{k=1}^n M_k$ denotes the block-diagonal matrix with M_k along the diagonal. This is usually denoted $\mathbf{diag}_k M_k$ for brevity. Similarly, for signals or vectors x_k , the notation $\mathbf{cat}_{k=1}^n x_k$ denotes the signal or vector (x_1, x_2, \dots, x_n) formed by concatenating x_k . This is also usually denoted $\mathbf{cat}_k x_k$ for brevity. The number of scalar components of a vector or signal x is denoted by $\mathbf{dim}(x)$.

II. PROBLEM FORMULATION

A. Spatially interconnected systems over an arbitrary graph

The plant to be controlled consists an assembly of L possibly different linear time-invariant continuous time subsystems. Let $V := \{1, 2, \dots, L\}$ be the set that indexes the subsystems. To each distinct pair of subsystems, indexed by i and j , we associate the following four signals - in the space \mathcal{L}_{2e} - flowing between them: 1) v_{ij} , the input of i coming from j , 2) w_{ij} , the output of i flowing towards j , and similarly, 3) v_{ji} and 4) w_{ji} . As mentioned in the introduction, $\mathbf{dim}(w_{ij}) = m_{ij}^+ = \mathbf{dim}(v_{ji}) = m_{ji}^-$. Letting $m_{ij}^+ = m_{ij}^- = 0$ will denote the fact that the subsystems i and j are not interconnected - neither system outputs a signal to the other.

Define the interconnection input v_i to each subsystem and the interconnection output w_i from each subsystem as the partitioned vectors $v_i := \mathbf{cat}_j(v_{ij})$ and $w_i := \mathbf{cat}_j(w_{ij})$ respectively. Each subsystem is described by the following state-space equations:

$$\begin{bmatrix} \dot{x}_i(t) \\ w_i(t) \\ z_i(t) \\ y_i(t) \end{bmatrix} = \begin{bmatrix} A_{TT_i} & A_{TS_i} & B_{T_i}^d & B_{T_i}^u \\ A_{ST_i} & A_{SS_i} & B_{S_i}^d & B_{S_i}^u \\ C_{T_i}^z & C_{S_i}^z & D_i^{zd} & D_i^{zu} \\ C_{T_i}^y & C_{S_i}^y & D_i^{yd} & 0 \end{bmatrix} \begin{bmatrix} x_i(t) \\ v_i(t) \\ d_i(t) \\ u_i(i) \end{bmatrix}. \quad (1)$$

The local state is x_i ; each subsystem outputs the sensor measurements y_i and is equipped with the control signal u_i . The local exogenous disturbance is d_i and the signals z_i represent the performance output in the standard \mathcal{H}_∞ formulation [20] (that is, we would like the signal $z := \mathbf{cat}_i z_i$ to have small \mathcal{L}_2 norm when $d := \mathbf{cat}_i d_i$ satisfies $\|d\| = 1$). Once the relationships between the inputs and outputs at each vertex have been defined, the distributed system can be described by closing all loops by imposing the constraints of interconnection. In other words, the condition that

$$v_{ij}(t) = w_{ji}(t) \quad (2)$$

is imposed at all times t , for all $i, j \in V$. We make the following assumptions on the plant matrices:

- 1) The control and measurement matrices of the closed loop system satisfy

$$B_{S_i}^u = D_i^{zu} = C_{S_i}^y = D_i^{yd} = 0. \quad (3)$$

These assumptions can always be satisfied by placing low-pass filters of sufficiently high bandwidth at the local measurement y_i and control input u_i as has been done in the linear parameter varying (LPV) control literature [1].

- 2) A subsystem must not have feed-through from neighboring subsystems:

$$A_{SS_i} = 0. \quad (4)$$

Thus the large-scale system must not have algebraic loops. This condition can be relaxed somewhat; see Remark 4.

- 3) The identity

$$\bar{\sigma}(F_i) < 1 \quad (5)$$

is assumed to hold, where

$$F_i := \begin{bmatrix} 0 & B_{S_i}^d \\ C_{S_i}^z & D_i^{zd} \end{bmatrix}. \quad (6)$$

Equation (5) is not required for stability robustness; see Remarks 2 and 3. Therefore, if this assumption is not satisfied, we can still guarantee that the closed loop system is robustly stable to arbitrarily small delays.

A controller for this system has subsystems K_i given by

$$\begin{bmatrix} \dot{x}_i^k(t) \\ w_i^k(t) \\ u_i(t) \end{bmatrix} = \begin{bmatrix} A_{TT_i}^k & A_{TS_i}^k & B_{T_i}^k \\ A_{ST_i}^k & A_{SS_i}^k & B_{S_i}^k \\ C_{T_i}^k & C_{S_i}^k & D_i^k \end{bmatrix} \begin{bmatrix} x_i^k(t) \\ v_i^k(t) \\ y_i(t) \end{bmatrix}. \quad (7)$$

It is important to note that no assumptions are made on the state-space matrices of the controller subsystems since there seems to be no way to impose sparsity of these matrices as a convex optimization problem. Notice that controller subsystems can communicate via the signals v_i^k and w_i^k . Each interconnection edge of the controller's graph is associated with the signals w_{ij}^k and v_{ij}^k such that $v_i^k = \mathbf{cat}_j v_{ij}^k$ and $w_i^k = \mathbf{cat}_j w_{ij}^k$. As explained in the introduction, $\mathbf{dim}(w_{ij}^k) = m_{ij}^{k,+}$ and $\mathbf{dim}(v_{ij}^k) = m_{ij}^{k,-}$. The

relation $v_{ij}^k = w_{ji}^k$, and consequently, $m_{ij}^{k,+} = m_{ji}^{k,-}$ always holds by definition. The closed loop subsystems are given by

$$\begin{bmatrix} \dot{x}_i^c(t) \\ w_i^c(t) \\ z_i(t) \end{bmatrix} = \begin{bmatrix} A_{TT_i}^c & A_{TS_i}^c & B_{T_i}^c \\ A_{ST_i}^c & A_{SS_i}^c & B_{S_i}^c \\ C_{T_i}^c & C_{S_i}^c & D_i^c \end{bmatrix} \begin{bmatrix} x_i^c(t) \\ v_i^c(t) \\ d_i(t) \end{bmatrix}, \quad (8)$$

where

$$x_i^c := (x_i, x_i^k), v_i^c := (v_i, v_i^k), w_i^c := (w_i, w_i^k) \quad (9)$$

The description of the closed loop system is completed by the equations

$$v_{ij}^c(t) = w_{ji}^c(t) \quad (10)$$

for each $i, j \in V$, where of course,

$$v_{ij}^c := (v_{ij}, v_{ij}^k), w_{ij}^c := (w_{ij}, w_{ij}^k) \quad (11)$$

and

$$A_{TT_i}^c := \begin{bmatrix} A_{TT_i} + B_{T_i}^u D_i^k C_{T_i}^y & B_{T_i}^u C_{T_i}^k \\ B_{T_i}^k C_{T_i}^y & A_{TT_i}^k \end{bmatrix}, \quad (12)$$

$$A_{TS_i}^c := \begin{bmatrix} A_{TS_i} & B_{T_i}^u C_{S_i}^y \\ 0 & A_{TS_i}^k \end{bmatrix}, A_{ST_i}^c := \begin{bmatrix} A_{ST_i} & 0 \\ B_{S_i}^k C_{T_i}^y & A_{ST_i}^k \end{bmatrix}, \quad (13)$$

$$A_{SS_i}^c := \begin{bmatrix} 0 & 0 \\ 0 & A_{SS_i}^k \end{bmatrix}, B_{T_i}^c := \begin{bmatrix} B_{T_i}^d \\ 0 \end{bmatrix}, B_{S_i}^c := \begin{bmatrix} B_{S_i}^d \\ 0 \end{bmatrix}, \quad (14)$$

$$C_{T_i}^c := [C_{T_i}^z \ 0], C_{S_i}^c := [C_{S_i}^z \ 0], D_i^c := D_i^{zd}. \quad (15)$$

Notice that in the above equations for the closed loop state space matrices, we have made the assumptions 1) and 2). Let the dimensions of $x_i^c(t)$, $w_{ij}^c(t)$ and $v_{ij}^c(t)$ be denoted respectively by n_i^c , $m_{ij}^{c,+}$ and $m_{ij}^{c,-}$. Define the matrices $X_{T_i} \in \mathbb{R}_{S^i}^{n_i^c}$, $X_{ij} \in \mathbb{R}_{S^i}^{m_{ij}^{c,+}}$, $Z_{ij} \in \mathbb{R}_{S^i}^{m_{ij}^{c,-}}$ and $Y_{ij} \in \mathbb{R}^{m_{ij}^{c,+} \times m_{ij}^{c,-}}$ satisfying the conditions

$$X_{T_i} \succ 0, X_{ij} = -Z_{ji}, Y_{ij}^* = -Y_{ji}. \quad (16)$$

(The identity $m_{ij}^{c,+} = m_{ji}^{c,-}$ ensures that the latter two conditions in (16) can hold).

The following simple analysis result for spatially interconnected systems is essentially from [11].

Theorem 1: [11] With the matrices X_{T_i} , X_{ij} , Y_{ij} and Z_{ij} as above, the closed loop system (8), (10) is well-posed [8], stable and satisfies the \mathcal{H}_∞ performance bound

$$\sup_{\|d\|=1} \|z\|^2 \leq (1 - \kappa) \quad (17)$$

for some $\kappa > 0$ if the quadratic form

$$\begin{aligned} & x_i^c(t)^* X_{T_i} x_i^c(t) + x_i^c(t)^* X_{T_i} x_i^c(t) + \sum_{j=1}^L \begin{bmatrix} w_{ij}^c(t) \\ v_{ij}^c(t) \end{bmatrix}^* \begin{bmatrix} X_{ij} & Y_{ij} \\ Y_{ij}^* & Z_{ij} \end{bmatrix} \\ & \times \begin{bmatrix} w_{ij}^c(t) \\ v_{ij}^c(t) \end{bmatrix} + z_i^*(t) z_i(t) - (1 - \kappa) d_i^*(t) d_i(t) \end{aligned} \quad (18)$$

is negative definite for each $i \in V$. Here $x_i^c(t)$, $v_{ij}^c(t)$ and $d_i(t)$ are free to take values in their respective linear spaces while $x_i^c(t)$, $w_{ij}^c(t)$ and $z_i(t)$ are computed from the subsystem equations (8).

Remark 1: A special case of the results in this paper appeared in [4]. The results of [4] assumed that $Y_{ij} = 0$ (which can be restrictive), and also only addressed the problem of stabilization (as opposed to the \mathcal{H}_∞ control problem addressed herein).

Theorem 2: [11] Given a plant (1), (2), a controller which renders the quadratic form (18) negative definite (for each $i \in V$) for some choice of suitable matrices X_{T_i} , X_{ij} , Y_{ij} and Z_{ij} can be computed as the solution of an LMI. Moreover, the dimensions of the controller interconnection signals can be bounded by $\max(m_{ij}^{k,+}, m_{ij}^{k,-}) \leq 3 \max(m_{ij}^+, m_{ij}^-)$.

B. Feedback loops with small delays: an abstract analysis problem

The interconnections described by equation (10) may be termed *ideal*, that is, they allow an instantaneous flow of information between any two subsystems. Since this assumption is rarely justified in practice, we would like to guarantee the stability and performance robustness of the closed loop system in the face of arbitrarily small delays between subsystems.

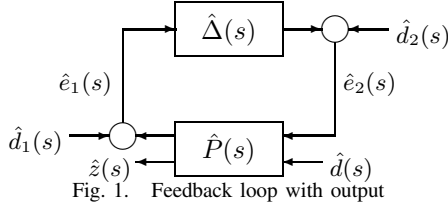


Fig. 1. Feedback loop with output

Definition 1: Consider a closed loop system with subsystems given by (8) and with the interconnections between subsystems described by

$$\hat{v}_{ij}^c(s) = \hat{\Delta}_{ij}(s)\hat{w}_{ji}^c(s) \quad (19)$$

instead of (10), where

$$\hat{\Delta}_{ij}(s) := \text{diag}_{k=1}^{m_{ji}^{c,+}} e^{-\epsilon_{ijk}s}. \quad (20)$$

($\hat{\cdot}$ denotes taking the Laplace transform). The system is said to exhibit robust stability and performance to small communication delays if there exists an $\epsilon > 0$ such that the system is internally stable [8] and satisfies (17) for some $\kappa > 0$ and for all $\epsilon_{ijk} \leq \epsilon$.

In this subsection, we describe the analysis results which will enable us to tackle the problem of synthesizing controllers that guarantee stability and performance of an interconnected system in the face of small communication delays between any two subsystems. The idea is to consider a linear fractional representation [15] consisting of an LTI system $\hat{P}(s)$ in feedback with a diagonal delay $\hat{\Delta}(s) := \text{diag}_{k=1}^N e^{-\epsilon_k s}$ (see Figure 1), and to develop analysis conditions which will guarantee robust stability and performance of this feedback system in the presence of any delay satisfying $\epsilon_k \leq \epsilon$ (for some $\epsilon > 0$). Since one can readily express an interconnected system with delays between subsystems as a linear fractional representation (by “pulling out the delays” as is done in robust control theory), such a result would be useful for synthesizing a controller which ensures this robustness property for the closed loop interconnected system given by (8), (19).

The analysis result presented in this subsection for feedback systems with small delays in the loop is essentially from [13].

Consider the feedback loop shown in Figure 1. Suppose the nominal feedback loop (i.e., with $\hat{\Delta}(s) = I$) is stable and satisfies (17) for some $\kappa > 0$. We would like to guarantee the existence of $\epsilon > 0$ such that the system shown in Figure 1 is internally stable (i.e., has uniformly bounded gain as an operator from (d_1, d_2) to (e_1, e_2)) and, with (d_1, d_2) set to zero, continues to satisfy (17) for all time delays $\hat{\Delta}(s) =: \text{diag}_{k=1}^N e^{-\epsilon_k s}$ such that $\epsilon_k \leq \epsilon$. The solution to this problem involves the computation of a structured singular value. Assume that $A \in \mathbb{C}^{(N+n) \times (N+m)}$. Define, for $\gamma > 0$, the set

$$\mathcal{D}_\gamma := \left\{ \left[\begin{array}{cc} \text{diag}_k \delta_k & 0 \\ 0 & \Theta \end{array} \right] : \delta_k \in \mathbb{C}, \Theta \in \mathbb{C}^{m \times n}, \max(|\delta_k|, \bar{\sigma}(\Theta)) \leq \gamma \right\} \quad (21)$$

and the structured singular value

$$\mu(A) := \frac{1}{\sup \{ \gamma > 0 : \det(I + AA') \neq 0, \forall A' \in \mathcal{D}_\gamma \}}. \quad (22)$$

The following theorem will suffice for our purposes.

Theorem 3: Assume that the feedback loop of Figure 1 is internally stable in the nominal case ($\hat{\Delta}(s) = I$) and satisfies (17). Then there exists an $\epsilon > 0$ such that

- 1) the induced norm from (d_1, d_2) to the internal signal (e_1, e_2) is uniformly bounded for all $\epsilon_k \leq \epsilon$
- 2) upon setting (d_1, d_2) is to zero, equation (17) continues to be satisfied by the feedback loop

if $\mu(\hat{P}(\infty)) < 1$.

Proof: The proof is a simple modification of the “if” part of Theorem 2.2 in the paper [13]; one uses the main loop theorem [15] to cast the robustness problem as a structured singular value test. The details are omitted for brevity. ■

Note that the condition that the induced norm from (d_1, d_2) to (e_1, e_2) is uniformly bounded amounts to *internal stability* of the delay system [8].

C. Linear fractional representation for interconnected systems

As mentioned in the previous subsection, we need to express the interconnected system as a feedback loop (Figure 1) by “pulling out the delays”. For each $i \in V$, let $G_i^c : \mathcal{L}_{2e} \rightarrow \mathcal{L}_{2e}$ be the subsystem represented by the state-space equations (8). Thus,

$$\begin{bmatrix} w_i^c \\ z_i \end{bmatrix} = G_i^c \begin{bmatrix} v_i^c \\ d_i \end{bmatrix} \quad (23)$$

(recall that v_i^c and w_i^c are interconnection signals concatenated as in (9)). We need the following Lemma, the proof of which is simple and is omitted.

Lemma 4: Suppose the interconnected system containing subsystems G_i^c , coupled as in Equations (19), (20), is represented as the feedback loop in Figure 1, where

$$\hat{\Delta} := \text{diag}_{i=1}^L \text{diag}_{j=1}^L \hat{\Delta}_{ij}(s). \quad (24)$$

Then

$$\hat{P}(s) = Q_1 (\text{diag}_i G_i^c(s)) Q_2, \quad (25)$$

where Q_1 and Q_2 are permutation matrices.

D. The control problem

It is now possible to precisely formulate our control problem as follows: *Given a plant whose subsystems are represented in state space by (1), find a controller represented by the equations (7) such that the closed loop is stable and satisfies (17) when the ideal (nominal) interconnection relation (11) is satisfied. In addition, ensure that the structured singular value condition $\mu(\hat{P}(\infty)) < 1$ is satisfied.* The following lemma allows us to reduce the problem to one involving the maximum singular value of the matrix $\hat{G}_i^c(\infty)$ rather than $\mu(\hat{P}(\infty))$ (this is convenient since the structured singular value is hard to compute [17]).

Lemma 5: The condition $\mu(\hat{P}(\infty)) < 1$ is satisfied provided $\bar{\sigma}(\hat{G}_i^c(\infty)) < 1$ for each $i \in V$.

Proof: Merely observe that permutation matrices are orthogonal, so that $\bar{\sigma}(\text{diag}_i \hat{G}_i^c(\infty)) = \bar{\sigma}(\hat{P}(\infty))$. Since $\mu(A) \leq \bar{\sigma}(A)$ for any matrix A [15], and since $\bar{\sigma}(\text{diag}_i \hat{G}_i^c(\infty)) = \max_i \bar{\sigma}(\hat{G}_i^c(\infty))$, the lemma follows immediately. ■

The observations of this section yield

Theorem 6: Suppose we can find a distributed controller that renders the closed loop system (8), (10) stable, with \mathcal{H}_∞ norm less than unity. Suppose also that the closed loop subsystems G_i^c satisfy $\bar{\sigma}(\hat{G}_i^c(\infty)) < 1$. From equations (14), (15), this last condition is equivalent to

$$\bar{\sigma}(\Xi_i) < 1, \quad (26)$$

where

$$\Xi_i := \hat{G}_i^c(\infty) = \begin{bmatrix} 0 & 0 & B_{S_i}^d \\ 0 & A_{SS_i}^k & 0 \\ C_{S_i}^z & 0 & D_i^z \end{bmatrix}, \quad (27)$$

for each $i \in V$. Then the closed loop exhibits robust stability and performance to small delays.

The following corollary for robust *stability* follows as a special case of the above result, noting that the closed loop feed-through matrix is $\text{diag}_i A_{SS_i}^k$, modulo some permutation matrices, when the exogenous inputs and outputs (d_i and z_i respectively) are absent.

Corollary 7: Given that the closed loop system exhibits nominal stability and performance, a sufficient condition for robust *stability* to small delays is the requirement $\bar{\sigma}(A_{SS_i}^k) < 1$.

Remark 2: The matrix Ξ_i is similar, via a permutation matrix, to

$$\tilde{\Xi}_i := \begin{bmatrix} A_{SS_i}^k & 0 \\ 0 & F_i \end{bmatrix} \quad (28)$$

(see equation (6)). Equation (26) can therefore be satisfied *only if* the assumption of equation (5) is satisfied. The precise condition on the matrix F_i which is necessary for robust performance in the presence of small delays involves the computation of a structured singular value; this point is not pursued further. However, Corollary 7 shows that robust stability to small delays can be obtained even if (5) is not satisfied.

We already know that a controller that stabilizes the nominal system can be found using the LMI analysis conditions of [11]; in addition, (26) must be satisfied. In the next section, we shall show how to obtain such a controller from the controller synthesized using the LMIs of [11].

III. STABILITY AND PERFORMANCE ROBUSTNESS AGAINST SMALL COMMUNICATION DELAYS

The main results of this paper are presented in this section. The proofs are collected in the Appendix.

A. Rewriting a controller for robustness against small delays

Theorem 3 provides the insight that if algebraic loops of gain ≥ 1 (in the sense of the structured singular value) are absent in the closed loop system, then it is robust to arbitrarily small time delays. This confirms and generalizes the intuition based on the simple example (a unity feedback around a gain of $q > 1$) described in the Introduction.

Now, we have assumed that the feed-through matrices A_{ss_i} between the plant subsystems are zero; see (4). This is justified in practice as a well-posedness assumption on the plant model and can sometimes be relaxed; see Remark 4. Algebraic loops in the closed loop are therefore caused by the controller feed-through matrices $A_{ss_i}^k$. However, one cannot simply assume that $A_{ss_i}^k$ are zero, since the controller matrices are obtained from the LMI conditions of [11], and imposing sparsity on controller matrices seems to be an intractable problem in general (the failure to cast decentralized control problems as convex optimization problems in general cases [3] seems to confirm this intuition). With the insight that algebraic loops of gain ≥ 1 destroy stability or performance in the presence of small delays however, one could try to *rewrite* the controller obtained from the *convex* synthesis conditions of [11] so that the hypothesis of Theorem 6 are satisfied by the *new* controller feed-through matrices. This is the procedure adopted in the sequel. Needless to say, the *plant should not be modified in any way* and the rewritten controller must also be distributed.

B. Controller construction

Suppose, therefore, that the controller (7) has been computed such that the quadratic form (18) is negative definite for each $i \in V$. Partition X_{ij} and Z_{ij} as

$$X_{ij} := \begin{bmatrix} X_{ij}^G & X_{ij}^{GK} \\ (X_{ij}^{GK})^* & X_{ij}^K \end{bmatrix}, Z_{ij} := \begin{bmatrix} Z_{ij}^G & Z_{ij}^{GK} \\ (Z_{ij}^{GK})^* & Z_{ij}^K \end{bmatrix} \quad (29)$$

so that the blocks conform to $w_{ij}^c = (w_{ij}, w_{ij}^k)$ and $v_{ij}^c = (v_{ij}, v_{ij}^k)$ respectively. Correspondingly, partition Y_{ij} as

$$Y_{ij} := \begin{bmatrix} Y_{ij}^G & Y_{ij}^{GK} \\ Y_{ij}^{KG} & Y_{ij}^K \end{bmatrix} \quad (30)$$

(note that Y_{ij} is not necessarily symmetric or even square).

The first step in the construction is to compute certain transformation matrices. Assume without loss of generality that the symmetric matrices

$$U_{ij}^K := \begin{bmatrix} X_{ij}^K & Y_{ij}^K \\ (Y_{ij}^K)^* & Z_{ij}^K \end{bmatrix} \quad (31)$$

are invertible and still result in (18) being negative definite (by a small perturbation if necessary). Compute matrices T_{ij} via a factorization

$$U_{ij}^K =: T_{ij}^* R_{ij} T_{ij}, \quad (32)$$

where

$$R_{ij} = \mathbf{diag}(I, -I). \quad (33)$$

Note that if R_{ij} has n_p positive entries and n_n negative entries, the inertia (the ordered triple consisting of the number of positive,

zero and negative eigenvalues respectively) of U_{ij}^K is $(n_p, 0, n_n)$. Make the co-ordinate transformation

$$\begin{bmatrix} w_{ij}^k \\ v_{ij}^k \end{bmatrix} =: T_{ij}^{-1} \begin{bmatrix} \tilde{w}_{ij}^k \\ \tilde{v}_{ij}^k \end{bmatrix}, \quad (34)$$

where the partition $(\tilde{w}_{ij}^k, \tilde{v}_{ij}^k)$ is conformable with the principal submatrices (I and $-I$ respectively) of R_{ij} . From the closed loop equations (8) and matrices (13), (14), we have

$$w_i^k = B_{S_i}^K C_{T_i}^y x_i + A_{S_{T_i}}^K x_i + A_{S_{S_i}}^K v_i^k \quad (35)$$

$$\Rightarrow [I - A_{S_{S_i}}^K] \mathcal{P}_i^k \mathbf{diag}_j T_{ij}^{-1} \left(\mathbf{cat}_j \begin{bmatrix} \tilde{w}_{ij}^k \\ \tilde{v}_{ij}^k \end{bmatrix} \right) = B_{S_i}^K C_{T_i}^y x_i + A_{S_{T_i}}^K x_i, \quad (36)$$

where \mathcal{P}_i^k is a permutation matrix. Define $\tilde{\mathcal{P}}_i^k$ by

$$\tilde{\mathcal{P}}_i^k : \begin{bmatrix} \mathbf{cat}_j \tilde{w}_{ij}^k \\ \mathbf{cat}_j \tilde{v}_{ij}^k \end{bmatrix} \mapsto \mathbf{cat}_j \begin{bmatrix} \tilde{w}_{ij}^k \\ \tilde{v}_{ij}^k \end{bmatrix}, \quad (37)$$

and define the matrix \mathcal{A}_i and the vector ζ_i by

$$\mathcal{A}_i := [I - A_{S_{S_i}}^K] \mathcal{P}_i^k \mathbf{diag}_j T_{ij}^{-1} \tilde{\mathcal{P}}_i^k \quad (38)$$

$$\zeta_i := B_{S_i}^K C_{T_i}^y x_i + A_{S_{T_i}}^K x_i \quad (39)$$

to get

$$\mathcal{A}_i \begin{bmatrix} \mathbf{cat}_j \tilde{w}_{ij}^k \\ \mathbf{cat}_j \tilde{v}_{ij}^k \end{bmatrix} = \zeta_i \quad (40)$$

$$\Rightarrow \mathcal{A}_i^w \mathbf{cat}_j \tilde{w}_{ij}^k = \mathcal{A}_i^v \mathbf{cat}_j \tilde{v}_{ij}^k + \zeta_i, \quad (41)$$

where $\mathcal{A}_i =: [\mathcal{A}_i^w \quad -\mathcal{A}_i^v]$ has been partitioned conformably with the vector $(\mathbf{cat}_j \tilde{w}_{ij}^k, \mathbf{cat}_j \tilde{v}_{ij}^k)$.

Thus if the matrix \mathcal{A}_i^w has a left inverse (it is in general rectangular), we can solve uniquely for $\mathbf{cat}_j \tilde{w}_{ij}^k$. The following theorem shows that this is indeed the case. For convenience, define $\tilde{w}_i^k := \mathbf{cat}_i \tilde{w}_{ij}^k$, $\tilde{v}_i^k := \mathbf{cat}_i \tilde{v}_{ij}^k$.

Theorem 8: The relation

$$\tilde{w}_i^k = (\mathcal{A}_i^w)^\dagger \mathcal{A}_i^v \tilde{v}_i^k + (\mathcal{A}_i^w)^\dagger \zeta_i \quad (42)$$

holds, where for a rectangular matrix Θ with full column rank, the pseudoinverse Θ^\dagger is defined as $(\Theta^* \Theta)^{-1} \Theta^*$.

Theorem 8 is a key result in the whole theory. This theorem has allowed us to define a new set of interconnection variables for the controller subsystems. We can now construct a new controller with these interconnection signals. From (42) and (39), we get

$$\tilde{w}_i^k = (\mathcal{A}_i^w)^\dagger \mathcal{A}_i^v v_i^k + (\mathcal{A}_i^w)^\dagger B_{S_i}^K C_{T_i}^y x_i + (\mathcal{A}_i^w)^\dagger A_{S_{T_i}}^K x_i. \quad (43)$$

Equation (43), together with the equation for the plant interconnection output w_i (obtained from (8)) now begins to resemble the closed-loop interconnection equation (with a new controller):

$$\begin{bmatrix} w_i \\ \tilde{w}_i^k \end{bmatrix} = \begin{bmatrix} A_{S_{T_i}} & 0 \\ \bar{B}_{S_i}^K C_{T_i}^y & \bar{A}_{S_{T_i}}^K \end{bmatrix} \begin{bmatrix} x_i \\ x_i^k \end{bmatrix} + \begin{bmatrix} 0 & 0 \\ 0 & \bar{A}_{S_{S_i}}^K \end{bmatrix} \begin{bmatrix} v_i \\ \tilde{v}_i^k \end{bmatrix} + \begin{bmatrix} B_{S_i}^d \\ 0 \end{bmatrix} d_i, \quad (44)$$

where

$$\bar{A}_{S_{T_i}}^K := (\mathcal{A}_i^w)^\dagger A_{S_{T_i}}^K, \quad \bar{A}_{S_{S_i}}^K := (\mathcal{A}_i^w)^\dagger \mathcal{A}_i^v, \quad \bar{B}_{S_i}^K := (\mathcal{A}_i^w)^\dagger B_{S_i}^K. \quad (45)$$

(Compare with (13), (14)).

Now the state equations of the closed loop subsystems are, for each $i \in V$,

$$\dot{x}_i^c = A_{T_{T_i}}^C x_i^c + A_{T_{S_i}}^C v_i^c + B_{T_i}^C d_i. \quad (46)$$

(See (8), (12), (13) and (14)). Since $v_i^c = (v_i, v_i^k)$, we must express v_i^k in terms of \tilde{v}_i^k . Partition the matrices T_{ij}^{-1} as

$$T_{ij}^{-1} =: S_{ij} = \begin{bmatrix} S_{ij}^{11} & S_{ij}^{12} \\ S_{ij}^{21} & S_{ij}^{22} \end{bmatrix} \quad (47)$$

according to (34). Some algebra later, we obtain

$$v_i^k = (\mathbf{diag}_j S_{ij}^{21}) \bar{B}_{S_i}^K C_{T_i}^y x_i + (\mathbf{diag}_j S_{ij}^{21}) \bar{A}_{S_{T_i}}^K x_i^k + [(\mathbf{diag}_j S_{ij}^{21}) \bar{A}_{S_{S_i}}^K + (\mathbf{diag}_j S_{ij}^{22})] \tilde{v}_i^k. \quad (48)$$

The closed loop state equation can now be written in terms of the states and the new interconnection input:

$$\begin{bmatrix} \dot{x}_i \\ \dot{x}_i^k \end{bmatrix} = \begin{bmatrix} A_{T_{T_i}} + B_{T_i}^u \bar{D}_i^K C_{T_i}^y & B_{T_i}^u \bar{C}_{T_i}^K \\ \bar{B}_{T_i}^K C_{T_i}^y & \bar{A}_{T_{T_i}}^K \end{bmatrix} \begin{bmatrix} x_i \\ x_i^k \end{bmatrix} + \begin{bmatrix} A_{T_{S_i}} & B_{T_i}^u \bar{C}_{S_i}^K \\ 0 & \bar{A}_{T_{S_i}}^K \end{bmatrix} \begin{bmatrix} v_i \\ \tilde{v}_i^k \end{bmatrix} + B_{T_i}^C d_i, \quad (49)$$

where the controller matrices $\bar{A}_{TS_i}^K$, $\bar{C}_{S_i}^K$, $\bar{B}_{T_i}^K$, $\bar{A}_{TT_i}^K$, $\bar{C}_{S_i}^K$, and $\bar{A}_{TS_i}^K$ can be computed from the foregoing signal relations. Comparing the closed loop matrices in (49) and (44) with (8), (12), (13) and (14), we see that the matrices $\bar{A}_{TT_i}^K$, etc, define a valid controller. The first theorem about this controller is that it achieves nominal stability and performance:

Theorem 9: Assume that the plant satisfies the (3) and (4). Suppose the controller subsystems (7) have been computed such that the quadratic form (18) is negative definite for each $i \in V$. Let the controller subsystems be as in Equation 7, with $A_{TT_i}^K$ replaced by $\bar{A}_{TT_i}^K$, etc. Let the closed loop subsystems be interconnected according to

$$v_{ij}(t) = w_{ij}(t) \text{ and } v_{ij}^K(t) = w_{ij}^K(t). \quad (50)$$

The resulting closed loop system is stable and satisfies (17).

The new closed loop system exhibits robust stability and performance to small delays (see Definition 1):

Theorem 10: Assume that the plant satisfies (3), (4) and (5). When the new controller is connected to the plant, the closed loop matrix $\bar{\Xi}_i$, which is just Ξ_i (see (27)) with $A_{SS_i}^K$ replaced by $\bar{A}_{SS_i}^K$, satisfies $\bar{\sigma}(\bar{\Xi}_i) < 1$. Consequently, by Theorems 9 and 6, the closed loop exhibits robust stability and performance to small delays.

Remark 3: From the proof of the above theorem (Section V-C), it is seen that $\bar{\sigma}(\bar{A}_{SS_i}^K) < 1$ whether or not (5) holds. It follows that the closed loop system satisfies the condition $\bar{\sigma}(A_{SS_i}^C) < 1$, where the closed loop feed-through matrix $A_{SS_i}^C$, is now given by equation (14) with $A_{SS_i}^K$ replaced by $\bar{A}_{SS_i}^K$. Thus from Corollary 7, the closed loop is robustly stable to small delays even if (5) is not satisfied.

Remark 4: The assumption $A_{SS_i} = 0$ made in this paper can be considerably relaxed if we are only concerned with stability robustness; the method given in this paper goes through without modifications if $\bar{\sigma}(A_{SS_i}) < 1$ for each $i \in V$. This can be seen from the proof of Theorem 10 and the fact that, when $A_{SS_i} \neq 0$, the closed loop feed-through matrix $A_{SS_i}^C$ (see (14)), with the new controller in place, becomes

$$A_{SS_i}^C = \begin{bmatrix} A_{SS_i} & 0 \\ 0 & \bar{A}_{SS_i}^K \end{bmatrix}. \quad (51)$$

From the previous remark, $\bar{\sigma}(\bar{A}_{SS_i}^K) < 1$ holds; therefore, $\bar{\sigma}(A_{SS_i}^C) < 1$. Robust stability now follows from Corollary 7.

Thus we see that the assumption of equation (5) is not necessary to guarantee *stability* robustness, which is encouraging, since one cannot really impose this constraint on the plant. Now suppose (5) does not hold, but that (3) and (4) do, and that a controller has been synthesized (as above) such that $\bar{\sigma}(\bar{A}_{SS_i}^K) < 1$. Thus, by Corollary 7, the system is robustly stable to small delays and exhibits nominal performance. Now the following question arises: is it possible to estimate an upper bound for the \mathcal{H}^∞ norm of the closed loop system from d to z in the presence of small delays? In other words, is it possible to find numbers $\Gamma, \epsilon > 0$ such that

$$\sup_{\|d\|=1} \|z\| < \Gamma \quad (52)$$

whenever the interconnections between subsystems are described by the relations (19) and (20), with $\epsilon_{ijk} < \epsilon$? Theorem 11 below answers this question in the affirmative.

Theorem 11: Suppose $F_i \in \mathbb{R}^{\tilde{m}_i \times \tilde{n}_i}$ (see (6) for the definition of F_i). Let $\bar{\sigma}(F_i) \geq 1$ for at least one $i \in V$, so that robust performance to small delays is not ensured by Theorem 10. Define

$$\Gamma := \max_i \{\tilde{n}_i \bar{\sigma}^2(F_i)\} + \eta, \quad (53)$$

where $\eta > 0$ is any (small) constant. Then there exists $\epsilon > 0$ such that the closed loop system obtained by interconnecting the plant with the new controller is internally stable and satisfies (52) whenever the interconnections between subsystems are described by the relations (19) and (20), with $\epsilon_{ijk} < \epsilon$.

Therefore, if the assumption (5) is not satisfied, it is still possible to bound the *worst-case* \mathcal{H}^∞ gain of the closed loop system in the presence of small delays, in terms of the plant matrices.

By chasing through the sizes of the signals, the following bound can be established on the dimensions of the controller's interconnection signals. The proof is simple and is omitted.

Theorem 12: Suppose the original controller (7) for the plant (1) satisfied the relation

$$\max(\mathbf{dim}(w_{ij}^K), \mathbf{dim}(v_{ij}^K)) \leq \alpha \max(\mathbf{dim}(w_{ij}), \mathbf{dim}(v_{ij})),$$

for some constant α . Then the new controller satisfies the relation

$$\max(\mathbf{dim}(w_{ij}^K), \mathbf{dim}(v_{ij}^K)) \leq 2\alpha \max(\mathbf{dim}(w_{ij}), \mathbf{dim}(v_{ij})).$$

Since we can guarantee a bound $\alpha = 3$ by the LMIs of [11], the new controller's interconnection dimension does not exceed that of the plant (in every channel) by more than a factor of 6.

IV. CONCLUSIONS

In this paper, a technique was given to construct a class of structured controllers to stabilize a spatially interconnected system and to render the closed loop \mathcal{H}^∞ norm less than unity in the face of small communication delays. The importance of this problem is self-evident given the spatially distributed nature of the problem considered in this and related work [11], [5]. The results build on those of [11] on distributed control synthesis for large-scale systems and on those of [13] on the stability of feedback loops affected by small delays. The controller construction algorithm has been demonstrated with the help of an example.

V. APPENDIX

A. Proof of Theorem 8

In view of the discussion before Theorem 8, it is sufficient to prove that \mathcal{A}_i^w has a left inverse.

Proof: Define $\Phi_i := (x_i^c)^* X_{T_i} x_i^c + (\hat{x}_i^c)^* X_{T_i} \hat{x}_i^c$. Note that while the state x_i^c is really a signal in \mathcal{L}_{2e} , we are treating it as a vector (in $\mathbb{R}^{n_i^c}$) here. We shall continue to do so for all other signals without further comment. Also, if $\beta(f)$ is a quadratic form in a vector f , we use the notation $\beta(f) < 0$ to convey the fact that β is negative definite (though the strict inequality holds only if $f \neq 0$).

Recall the meaning of the negative definiteness of the quadratic form (18): vectors x_i^c , v_i^c and d_i are independent variables and \hat{x}_i^c , w_i^c and z_i must be computed from the closed loop equations (8) and (12) - (15). Now, if we set $x_i = x_i^k = v_{ij} = d_i = 0$, we see from (8) and (12) - (15) that $w_{ij} = z_i = 0$; so (18) becomes

$$\sum_{j=1}^L \begin{bmatrix} w_{ij}^K \\ v_{ij}^K \end{bmatrix}^* U_{ij}^K \begin{bmatrix} w_{ij}^K \\ v_{ij}^K \end{bmatrix} < 0, \quad (54)$$

where U_{ij}^K was defined in (31). From (34), (32) and (33), we have

$$\sum_{j=1}^L |\tilde{w}_{ij}^K|^2 - \sum_{j=1}^L |\tilde{v}_{ij}^K|^2 < 0 \implies |\tilde{w}_i^K|^2 - |\tilde{v}_i^K|^2 < 0. \quad (55)$$

From equation (41), noting that $\zeta_i = 0$ (this is because we have set $x_i = x_i^k = 0$), we have

$$\mathcal{A}_i^w \tilde{w}_i^K = \mathcal{A}_i^v \tilde{v}_i^K. \quad (56)$$

From (55) and (56), we have that, for all ω and ν satisfying $\mathcal{A}_i^w \omega - \mathcal{A}_i^v \nu = 0$, the inequality $|\omega|^2 - |\nu|^2 < 0$ holds provided $|\omega|^2 + |\nu|^2 \neq 0$. Now suppose $\mathcal{A}_i^w \omega = 0$ for some $\omega \neq 0$. Then by choosing $\nu = 0$, we can satisfy $\mathcal{A}_i^w \omega - \mathcal{A}_i^v \nu = 0$, but on the other hand, $|\omega|^2 - |\nu|^2 = |\omega|^2 > 0$, a contradiction. Hence $\mathcal{A}_i^w \omega = 0$ implies that $\omega = 0$. Thus \mathcal{A}_i^w has full column rank; in other words it has a left inverse. ■

B. Proof of Theorem 9

From (18), (34), (32) and (47), we have

$$\Phi_i + \sum_{j=1}^L \begin{bmatrix} \tilde{w}_{ij}^C \\ \tilde{v}_{ij}^C \end{bmatrix}^* \begin{bmatrix} \tilde{X}_{ij} & \tilde{Y}_{ij} \\ \tilde{Y}_{ij}^* & \tilde{Z}_{ij} \end{bmatrix} \begin{bmatrix} \tilde{w}_{ij}^C \\ \tilde{v}_{ij}^C \end{bmatrix} + z_i^* z_i - (1 - \kappa) d_i^* d_i < 0, \quad (57)$$

where $\tilde{w}_{ij}^C := (w_{ij}, \tilde{w}_{ij}^K)$, $\tilde{v}_{ij}^C := (v_{ij}, \tilde{v}_{ij}^K)$ and

$$\tilde{X}_{ij} := \begin{bmatrix} X_{ij}^G & \star \\ (S_{ij}^{11})^* (X_{ij}^{GK})^* + (S_{ij}^{21})^* (Y_{ij}^{GK})^* & I \end{bmatrix}, \quad (58)$$

$$\tilde{Y}_{ij} := \begin{bmatrix} Y_{ij}^G & Y_{ij}^{GK} S_{ij}^{22} + X_{ij}^{GK} S_{ij}^{12} \\ (S_{ij}^{11})^* Y_{ij}^{GK} + (S_{ij}^{21})^* (Z_{ij}^{GK})^* & 0 \end{bmatrix}, \quad (59)$$

$$\tilde{Z}_{ij} := \begin{bmatrix} Z_{ij}^{G_j} & \star \\ (S_{ij}^{22})^* (Z_{ij}^{G_j})^* + (S_{ij}^{12})^* Y_{ij}^{KG} & -I \end{bmatrix}, \quad (60) \quad \implies \|F'_i\|_F < \frac{\|F_i\|_F}{\bar{\sigma}(F_i)\sqrt{\bar{n}_i}}. \quad (70)$$

where \star denotes terms induced by symmetry. Recall that $\mathbf{dim}(w_{ij}^K) = m_{ij}^{K,+}$ and $\mathbf{dim}(v_{ij}^K) = m_{ij}^{K,-}$; let $\mathbf{dim}(\tilde{w}_{ij}^K) := \tilde{m}_{ij}^{K,+}$ and $\mathbf{dim}(\tilde{v}_{ij}^K) := \tilde{m}_{ij}^{K,-}$. From (16), we have $X_{ij}^K = -Z_{ji}^K$ and $(Y_{ij}^K)^* = -Y_{ji}^K$. These can be written as

$$\begin{bmatrix} X_{ij}^K & Y_{ij}^K \\ (Y_{ij}^K)^* & Z_{ij}^K \end{bmatrix} = \begin{bmatrix} 0 & I_{m_{ij}^{K,-}} \\ I_{m_{ij}^{K,+}} & 0 \end{bmatrix} \left(- \begin{bmatrix} X_{ij}^K & Y_{ij}^K \\ (Y_{ij}^K)^* & Z_{ij}^K \end{bmatrix} \right) \times \begin{bmatrix} 0 & I_{m_{ij}^{K,+}} \\ I_{m_{ij}^{K,-}} & 0 \end{bmatrix} \quad (61)$$

$$\implies \begin{bmatrix} X_{ij}^K & Y_{ij}^K \\ (Y_{ij}^K)^* & Z_{ij}^K \end{bmatrix}^{-1} = \begin{bmatrix} 0 & I_{m_{ij}^{K,-}} \\ I_{m_{ij}^{K,+}} & 0 \end{bmatrix} \left(- \begin{bmatrix} X_{ij}^K & Y_{ij}^K \\ (Y_{ij}^K)^* & Z_{ij}^K \end{bmatrix}^{-1} \right) \times \begin{bmatrix} 0 & I_{m_{ij}^{K,+}} \\ I_{m_{ij}^{K,-}} & 0 \end{bmatrix}. \quad (62)$$

Thus $(U_{ij}^K)^{-1}$ and $-(U_{ji}^K)^{-1}$ have the same inertia (see (31)). From (32) and (47), the identity

$$(U_{ij}^K)^{-1} = S_{ij} R_{ij} S_{ij}^* \quad (63)$$

holds. Since the inertia of $(U_{ij}^K)^{-1}$ is captured by R_{ij} , we can in fact compute S_{ij} via (32), (47) for $i < j$ and get S_{ij} for $i > j$ from

$$\begin{bmatrix} S_{ij}^{11} & S_{ij}^{12} \\ S_{ij}^{21} & S_{ij}^{22} \end{bmatrix} = \begin{bmatrix} 0 & I_{m_{ij}^{K,+}} \\ I_{m_{ij}^{K,-}} & 0 \end{bmatrix} \begin{bmatrix} S_{ji}^{11} & S_{ji}^{12} \\ S_{ji}^{21} & S_{ji}^{22} \end{bmatrix} \begin{bmatrix} 0 & I_{m_{ij}^{K,+}} \\ I_{m_{ij}^{K,-}} & 0 \end{bmatrix}. \quad (64)$$

Assume that this has been done prior to controller construction. Thus

$$\begin{bmatrix} S_{ji}^{11} & S_{ji}^{12} \\ S_{ji}^{21} & S_{ji}^{22} \end{bmatrix} = \begin{bmatrix} S_{ij}^{22} & S_{ij}^{21} \\ S_{ij}^{12} & S_{ij}^{11} \end{bmatrix} \quad (65)$$

Substituting equations (16), (65) into (58), (59) and (60) gives

$$\tilde{X}_{ij} = -\tilde{Z}_{ji}, \tilde{Y}_{ij}^* = -\tilde{Y}_{ji}. \quad (66)$$

The theorem now follows from (57), (66) the stability criterion given in Theorem 1.

C. Proof of Theorem 10

From (45), the controller matrix $\bar{A}_{ss_i}^K$ is nothing but $(A_i^w)^\dagger A_i^v$. We claim that $\bar{\sigma}(\bar{A}_{ss_i}^K) < 1$. We know that for all ω, ν such that $A_i^w \omega = A_i^v \nu$, the inequality $|\omega|^2 < |\nu|^2$ holds provided ω and ν are not both zero (see the proof of Theorem 8). Thus for all $\nu \neq 0$, it holds that $|(A_i^w)^\dagger A_i^v \nu|^2 < \nu^2$, or that $\bar{\sigma}(\bar{A}_{ss_i}^K) < 1$. From (5) and the structure of the matrix $\bar{\Xi}_i$ (see Remark 2 and (28) for example), the theorem follows.

D. Proof of Theorem 11

Note that, since $\bar{\sigma}(F_i) \geq 1$ for at least one $i \in V$, the identity $\Gamma > 1$ holds. Define a new closed loop system by scaling the outputs z_i and the inputs d_i in (8) as follows:

$$z'_i := \frac{z_i}{\sqrt{\Gamma}}, d'_i := d_i \sqrt{\Gamma}, \quad (67)$$

where z'_i and d'_i are the new output and input respectively of subsystem i .

In terms of the new output and input, the matrix F_i in (6) is transformed to

$$F'_i := \begin{bmatrix} 0 & \frac{B_{S_i}^d}{\sqrt{\Gamma}} \\ \frac{C_{S_i}^z}{\sqrt{\Gamma}} & \frac{D_i^z d}{\Gamma} \end{bmatrix}. \quad (68)$$

We claim that $\bar{\sigma}(F'_i) < 1$. Assume that $F'_i \neq 0$. Recall that the Frobenius norm of any matrix A , denoted by $\|A\|_F$, is given by the Euclidean norm of the vector of all elements of A . Since $\Gamma > 1$,

$$\|F'_i\|_F \leq \frac{\|F_i\|_F}{\sqrt{\Gamma}} \quad (69)$$

Since $\|\bullet\|_F$ and $\bar{\sigma}(\bullet)$ are equivalent norms satisfying

$$\bar{\sigma}(A) \leq \|A\|_F \leq \bar{\sigma}(A)\sqrt{\bar{n}} \quad (71)$$

for any $A \in \mathbb{R}^{m \times n}$ (see for example [9], page 56), one obtains

$$\|F'_i\|_F < 1 \quad (72)$$

$$\implies \bar{\sigma}(F'_i) < 1, \quad (73)$$

which proves the claim. Now the nominal system mapping $d' := \mathbf{cat}_i d'_i$ to $z' := \mathbf{cat}_i z'_i$ (is stable and) has \mathcal{H}_∞ norm less than unity, since

$$\frac{\|z'\|}{\|d'\|} = \frac{1}{\Gamma} \frac{\|z\|}{\|d\|} \leq \frac{\sqrt{1-\kappa}}{\Gamma} \quad (74)$$

whenever $d \neq 0$; the last inequality is because the nominal system satisfies the performance equation (17) for some $\kappa > 0$. Since $\bar{\sigma}(\bar{A}_{ss_i}^K) < 1$ by hypothesis, Theorem 10 and the form of equation (28) imply that the system mapping d' to z' exhibits robust performance to small time delays; thus

$$\sup_{\|d'\|=1} \|z'\| < 1 \quad (75)$$

in the presence of small delays. From the first equality of (74), the theorem follows.

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