

Reachability Analysis for N-Squared State Charts over a Boolean Semiring Applied to a Hysteretic Discrete Event Structural Control Model

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Abstract—This paper presents reachability results for a class of discrete event systems modelled by industry-standard N-squared diagrams. These diagrams represent state transition charts using Boolean variables and functions, and form the basis for the design of hierarchical discrete event controllers used to operate, monitor and protect complex servomechanism systems. The presence of Boolean variables is addressed by means of dynamical systems modelled over semirings. Four distinct system descriptions capture the very subtle transformation from the physical problem to “standard” reachability constructions. The conditions are in a state space form and permit analytical assessment of plant and closed-loop system properties prior to coding in software, simulation and laboratory testing, thus offering immense savings in verification time and cost.

I. INTRODUCTION

This paper builds on the state space discrete event modelling in [1] based upon N^2 diagrams by applying the ideas in Massey and Sain [2] to obtain qualitative reachability criteria for a family of discrete event systems. The state space model presented in [1] has close parallels to classical linear system theory, except that because it describes logical dynamics, the model is defined over a Boolean semiring instead of a field such as the real numbers.

N-squared diagrams are an industry standard for modelling complex discrete event system behavior, and provide a basis for the mode and fault control designs in autonomous and semi-autonomous systems. Their matrix-like structure readily leads to state representations, supporting qualitative assessment of discrete event control system properties.

A major question for discrete event control system (DECS) design is whether operational states exist that cannot be entered under any conditions, or if there are states that once entered, cannot be left. Present state of the art relies upon expensive laboratory testing and is often incomplete. The results herein detail a method for analytically approaching such questions for DECS described by N^2 diagrams, greatly reducing the need for laboratory testing.

The sequel introduces N^2 diagrams using an example arising from the application of a hysteretic magnetorheological damper (MRD) to a 3DOF structure to mitigate responses to seismic excitation [1]. Next, the algebraic tools

for casting DECS models into a state space form over a Boolean semiring are presented. Finally, the results in [2] are extended to obtain the desired reachability criteria.

II. N^2 DIAGRAM DEFINITION USING AN MRD

The state space discrete event model presented herein was motivated while investigating means of controlling a three degree of freedom (3DOF) structure during seismic excitation. The nonlinear controller considered utilized a hysteretic magnetorheological damper (MRD) to control the structure’s effective damping, a so-called semi-passive approach. The equations of motion for the structure are detailed by Dyke *et al.* [3], [4], but only the discrete event portion of the model is presented here.

The hysteretic nature of the MRD is represented using a second-order modified Bouc-Wen hysteresis model [5] described by a differential equation of the form

$$\dot{z} = (a - b_1 z^2)\dot{x}, \quad \text{sgn } z = \text{sgn } \dot{x}, \quad (1)$$

$$\dot{z} = (a + b_2 z^2)\dot{x}, \quad \text{sgn } z \neq \text{sgn } \dot{x}, \quad (2)$$

where a , b_1 , b_2 , x and z are real scalars, x represents displacement of the MRD and the first story, z is a nonphysical variable representing the hysteretic portion of the restoring force applied by the MRD, and a , b_1 and b_2 are loop-shaping parameters for the hysteresis. The state chart for the MRD model is shown in Fig. 1.

For a given model having a set of n distinct operating conditions, let the corresponding discrete event model have n unique states. The corresponding N^2 diagram is an $n \times n$ grid with the n discrete states of the model placed on the squares on the main diagonal of the grid. The first or entrance state is placed in the upper left corner and the exit or final state is placed in the lower right corner. Intermediate states can be placed in any order along the main diagonal. Fig. 2 illustrates placement of the states in Fig. 1 into an N^2 diagram (entries A1, B2, C3 and D4).

Transitions are represented on the off-diagonal elements in an N^2 diagram. Let s_i , $i = 1, \dots, n$, denote the discrete states. Each transition is uniquely associated with a single *source* state and a single *destination* state. Given a unique pair of states (s_i, s_j) , where $i \neq j$, let s_i denote the source state, and s_j denote the destination state. The transition associated with this pair, denoted t_{ij} , is placed in grid location (i, j) of the N^2 diagram. Fig. 2 illustrates placement of the transitions shown in the state chart in Fig. 1 into an N^2 diagram.

Transitions in Fig. 1 having the same source and destination state are shown only for completeness’ sake and

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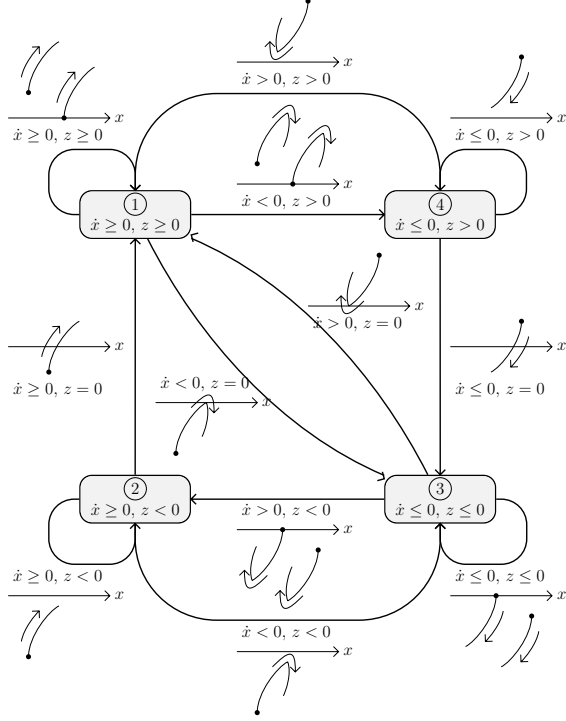


Fig. 1. State chart for a hysteretic MRD attached to a 3DOF structure.

	1	2	3	4
A	s_1		t_{13}	t_{14}
B	t_{21}	s_2	t_{23}	
C	t_{31}	t_{32}	s_3	
D	t_{41}		t_{43}	s_4

Fig. 2. N^2 diagram for hysteretic MRD model.

are omitted in the N^2 diagram. Also, this work assumes all relevant operating conditions of the modelled system are represented in the N^2 diagram.

Let s_i represent the state that corresponds to the present operating condition of the system. The system is then *in state* s_i , meaning the logical relations for state s_i are *true* (see Table I). Equivalently, s_i is *active*. The system changes to state s_j if and only if an event occurs such that the conditions associated with transition t_{ij} evaluate *true* (see Table II), assuming t_{ij} exists; in this situation, transition t_{ij} is said to be *active*. Note \wedge and \vee represent the logical *and* and *or* operations.

In the sequel, a serial process will be assumed. Extension of the model to parallel processes is reasonably straightforward, but beyond the scope of this paper. The following modelling assumptions for N^2 diagrams are designed to facilitate control under fault conditions by eliminating ambiguity with respect to active states and transitions and also to admit a specialized state space representation [6].

Assumption 1 Each state is unique.

Assumption 2 At most one state is active at a time.

Assumption 3 At most one transition is active at a time.

TABLE I
MRD N^2 STATE TABLE

State	Description
s_1	$(\dot{x} \geq 0) \wedge (z \geq 0)$
s_2	$(\dot{x} \geq 0) \wedge (z < 0)$
s_3	$(\dot{x} \leq 0) \wedge (z \leq 0)$
s_4	$(\dot{x} \leq 0) \wedge (z > 0)$

TABLE II
MRD N^2 TRANSITION TABLE

Transition	Description
t_{13}	$(\dot{x} < 0) \wedge (z = 0)$
t_{14}	$(\dot{x} \leq 0) \wedge (z > 0)$
t_{21}	$(\dot{x} \geq 0) \wedge (z = 0)$
t_{23}	$(\dot{x} < 0) \wedge (z < 0)$
t_{31}	$(\dot{x} > 0) \wedge (z = 0)$
t_{32}	$(\dot{x} > 0) \wedge (z < 0)$
t_{41}	$(\dot{x} > 0) \wedge (z > 0)$
t_{43}	$(\dot{x} \leq 0) \wedge (z = 0)$

Assumption 4 Ideally, the time interval over which a transition is active has measure zero.

If the system is in a given state s_i , and the logical relations associated with it evaluate *false*, then the system must change state. The new state is determined by examining the transitions t_{ij} , $1 \leq j \leq n$, $i \neq j$. If, for example, transition t_{ik} evaluates *true*, then the system has changed to state s_k . Changes requiring noneligible time intervals are modelled by creating a state that is active during the change.

At this point, a constructive approach for relating a specialized state space realization to the classical N^2 diagram and associated state charts has been demonstrated. The appeal of the state space representation is that classical controls concepts like stability, reachability and detectability are readily formulated in a rigorous manner. The latter two concepts are of particular interest in discrete event control designs for operational mode and fault management. They provide a means of assessing whether a system can get into and out of operational modes as required, and also whether one can determine or observe what conditions triggered a particular response from the controller. In the sequel, results for reachability for such systems are presented, following the mathematical framework required to work within a Boolean semiring.

III. MATHEMATICAL PRELIMINARIES FOR SYSTEMS OVER A BOOLEAN SEMIRING

A *semigroup* (S, \square) is a set S together with a binary operation $\square : S \times S \rightarrow S$ which is associative. A *monoid* (M, \square, e_M) is a semigroup (M, \square) with the unit element e_M for \square , i.e. $e_M \square x = x \square e_M = x$ for all $x \in M$. A *semiring* $R = (R, \square, e_R, \circ, 1_R)$ is a set R with two binary operations, box \square and circle \circ , such that, (R, \square, e_R) is a commutative monoid under \square ; $(R, \circ, 1_R)$ is a monoid under \circ ; $r \circ e_R = e_R = e_R \circ r$ for all $r \in R$; circle \circ is distributive on both sides over box \square . A *commutative semiring* is a semiring in which the operator \circ is commutative. A commutative semiring R is idempotent if $a \square a = a$, for all $a \in R$. A Boolean set $B_L = (\{0, 1\}, \vee, 0, \wedge, 1)$ with the logic operator OR (\vee) and the logic operator AND (\wedge) is a commutative idempotent semiring. Define a set \bar{B}_L^n as the set of vectors with n entries in B_L and $G = \{e_1, \dots, e_n\}$ as the subset of \bar{B}_L^n , where $e_1 = [1, 0, \dots, 0]^T$,

$\mathbf{e}_2 = [0, 1, \dots, 0]^T, \dots$, and $\mathbf{e}_n = [0, 0, \dots, 1]^T$. The scalar multiplication on $\mathbf{e}_i = [e_{1i}, e_{2i}, \dots, e_{ni}]^T$ is defined as $k\mathbf{e}_i = [k \wedge e_{1i}, k \wedge e_{2i}, \dots, k \wedge e_{ni}]^T$, where k is in B_L and \mathbf{e}_i has only one nonzero entry e_{ii} , $\forall i \in \{1, \dots, n\}$. The set B_L^n is defined as the set of all linear combinations of elements \mathbf{e}_i in G under the logic OR operators, $B_L^n = \{\bigvee_{i=1}^n k_i \mathbf{e}_i : k_i \in B_L\}$. Notice that the elements in B_L^n are sequences of arbitrary order of \mathbf{e}_i in G , concatenated by the logic OR operators. We define a binary operator \vee_1 on B_L^n as the ordinary OR operator, i.e. for any two elements b and b' in B_L^n , where $b = \bigvee_{i=1}^n k_i \mathbf{e}_i$ and $b' = \bigvee_{j=1}^n k'_j \mathbf{e}_j$, we have

$$b \vee_1 b' = \left(\bigvee_{i=1}^n k_i \mathbf{e}_i \right) \vee \left(\bigvee_{j=1}^n k'_j \mathbf{e}_j \right).$$

Therefore, $(B_L^n, \vee_1, \mathbf{0})$ is a monoid and $\mathbf{0}$ is the zero vector. Notice that the vectors in any sequence of B_L^n have no multiple nonzero entries, while the set \bar{B}_L^n permits the multiple nonzero entries. The reason to distinguish these two sets is that the system model of the MRD only allows the state vector to have one nonzero entry. We can define a componentwise logic operator \vee_2 on \bar{B}_L^n . For any two elements \bar{b}_1 and \bar{b}_2 and $\bar{b}_1 = [\bar{b}_{11}, \bar{b}_{21}, \dots, \bar{b}_{n1}]^T$ and $\bar{b}_2 = [\bar{b}_{12}, \bar{b}_{22}, \dots, \bar{b}_{n2}]^T$, we have

$$\bar{b}_1 \vee_2 \bar{b}_2 = [\bar{b}_{11} \vee \bar{b}_{12}, \bar{b}_{21} \vee \bar{b}_{22}, \dots, \bar{b}_{n1} \vee \bar{b}_{n2}]^T.$$

Therefore, $(\bar{B}_L^n, \vee_2, \mathbf{0})$ is also a monoid. Moreover, there exists an isomorphic map from \bar{B}_L^n to B_L^n . This is because any vector $\bar{b} = [\bar{b}_1, \bar{b}_2, \dots, \bar{b}_n]^T$ in \bar{B}_L^n is isomorphic to an element in B_L^n and vice versa, i.e.

$$\bar{b} \cong \begin{bmatrix} \bar{b}_1 \\ 0 \\ \vdots \\ 0 \\ 0 \end{bmatrix} \vee \begin{bmatrix} 0 \\ \bar{b}_2 \\ \vdots \\ 0 \\ 0 \end{bmatrix} \vee \dots \vee \begin{bmatrix} 0 \\ 0 \\ \vdots \\ 0 \\ \bar{b}_n \end{bmatrix}.$$

If we have $A = (a_{ij}) \in \bar{B}_L^{n \times n}$ and $\mathbf{e}_i \in B_L^n$ with only one nonzero entry e_{ii} , then the matrix multiplication $\wedge_1 : B_L^n \rightarrow B_L^n$ can be defined as

$$A \wedge_1 \mathbf{e}_i = \bigvee_{l=1}^n [0, \dots, a_{li}, \dots, 0]^T.$$

Because an arbitrary element $b \in B_L^n$ is a linear combination of these generators \mathbf{e}_i , the matrix multiplication can be easily derived. If we have matrices $\bar{A} = (\bar{a}_{ij}) \in \bar{B}_L^{n \times n}$ and $\bar{b} \in \bar{B}_L^n$, then the componentwise matrix multiplication $\wedge_2 : \bar{B}_L^n \rightarrow \bar{B}_L^n$ is defined by $(\bar{A} \wedge_2 \bar{b})_i = \bigvee_{k=1}^n (\bar{a}_{ik} \wedge \bar{b}_k)$ for $i = 1, \dots, m$. See Section V-C for a qualitative description of the difference between the ideas above.

IV. PHYSICAL SYSTEM REPRESENTATION

A. Physical System Model Σ_1

Using the N-squared diagram, the discrete event dynamics of the MRD can be written as a discrete-time linear

system Σ_1 over a Boolean semiring B_L ,

$$x(k+1) = A(k)x(k) \vee_1 b u(k), \quad (3)$$

where k represents the occurrence of the k -th event; the state x is in $X = B_L^n$ and the control u is in $U = B_L$; $A \in \bar{B}_L^{n \times n}$ is a matrix mapping $A : X \rightarrow X$ and $b \in B_L^n$ is a matrix mapping $b : U \rightarrow X$. The matrix multiplication here is \wedge_1 . The construction of $A(k)$ depends on the continuous dynamics, $\dot{x}_p = f(x_p, t, u_p)$, at each time $t_k \geq 0$, $t_k \in \mathbb{R}$, where t_k is the occurrence time of the k -th event. Therefore, we need to evaluate the truth table of the N^2 diagram to obtain $A(k)$. For instance, if, starting from a node i , the continuous state x_p satisfies the logic conditions in node s_i in the N^2 diagram, then we enter 1 in $A_{i,i}(k)$ and enter 0 elsewhere. If the logic conditions at node s_i are false, then we evaluate the transition condition t_{ij} from node i to j in the N -squared diagram. If it is true, then we enter 1 in $A_{j,i}(k)$ and enter 0 elsewhere. In other words, the matrix $A(k)$ not only depends on k but also the continuous state vectors of the physical plant. The construction of b can be determined by setting $b_i = 1$ if s_i is the initial node and all other entries in b to zero. The input $u(k) = 1$ for $k = 0$, and is zero thereafter. The details of the construction of A and b matrices are in [6].

B. Reachability of System Σ_1

System Σ_1 models the discrete event behavior of the MRD and the matrices in system Σ_1 depend on continuous dynamics. Therefore, the reachability definition for Σ_1 is different from that in standard linear system theory [7]. We are given $x_i(k_1), x_j(k_2) \in X$, with nonzero entries only at the i -th and j -th rows, respectively; i.e. the node i and the node j are active in the N -squared diagram, respectively. If $i \neq j$, then $x_j(k_2)$ is said to be 1-step reachable from $x_i(k_1)$ under the system Σ_1 if, for $t_{k_1} \leq t \leq t_{k_1+1}$, there exists a continuous control u_p such that x_p satisfies the transition condition t_{ij} at time $t = t_{k_1+1}$, and no feasible transition condition prior to that time, so that $k_2 = k_1 + 1$. If $j = i$, then, at the next event $k_1 + 1$, the continuous state x_p has to satisfy the logic condition s_i in the N -squared diagram, i.e. s_i is true and $k_2 = k_1 + 1$. This is equivalent to saying that the node i is 1-step reachable from the node i in the N^2 diagram. We say that $x_j(k_{n+1})$ is n -step reachable from $x_i(k_1)$ under the system Σ_1 if there exists a sequence of states

$$\{x_1(k_1 + 1), x_2(k_1 + 2), \dots, x_{n-1}(k_1 + n - 1)\} \subset X,$$

in which $x_1(k_1 + 1)$ is 1-step reachable from $x_i(k_1)$ and $x_{r+1}(k_1 + r + 1)$ is 1-step reachable from $x_r(k_1 + r)$, $r \in \{1, 2, \dots, n - 2\}$, $x_j(k_{n+1})$ is 1-step reachable from $x_{n-1}(k_1 + n - 1)$, and $k_{n+1} = k_1 + n$. The reachability of the system Σ_1 is equivalent to the reachability of nodes in the N^2 diagram. A similar definition can also be found in the hybrid systems literature [8].

Because the entries of $A(k)$ depend on the continuous dynamics, we cannot compute the set of reachable states

from an initial state without knowing the continuous states. However, if there exists a transition condition t_{ij} in the N^2 diagram, then there is a possibility the node j can be reached from the node i . If there exists no transition condition at the intersection of the i -th row and the j -th column in the N^2 diagram, then we can say the node j can never be directly reached from the node i . Along this line, we are able to identify a subset X^* of X , in which the states might be reached if the continuous dynamics can reach the transition regions. For any element x in X^* , we can say that there exists a sequence of transition conditions from the initial state to x , and x could possibly be reached, provided that the transition conditions are satisfied. For the state x in X but not in X^* , there is never a sequence of transitions which could drive an initial state to x . The subset of such states is denoted by X/X^* , which contains all unreachable states of X , in the sense of Σ_1 .

V. TIME-INVARIANT SYSTEM REPRESENTATION

A. Simplified System Model Σ_2

The system model for the physical MRD system is a time-varying system; so it is less convenient to study the reachability of such a system. If we assume that the continuous dynamics in the MRD are locally controllable, i.e. any transition conditions can be satisfied at appropriate time instants, then the system Σ_1 can be simplified into a time-invariant linear system Σ_2 over B_L ,

$$\hat{x}(k+1) = \hat{A} \hat{x}(k) \vee_1 \hat{b} \hat{u}(k), \quad (4)$$

where the state \hat{x} is in $\hat{X} = B_L^n$ and the control $\hat{u} \in \hat{U} = B_L$, the \hat{A} matrix is the transpose of the truth table of the N -squared diagram of this discrete-event model and $\hat{b} = b$.

B. Reachability of System Σ_2

Because the system Σ_2 is independent of the continuous dynamics, the reachability definition in ([9], p.158) can be extended to the time-invariant linear system Σ_2 over B_L . The sequence of reachable sets \hat{X}_i under the control \hat{u} of the system Σ_2 has the representation $\bigvee_{j=0}^{i-1} \hat{A}^{i-1-j} \hat{b}_j$, for the elements $\hat{b}_j \in \text{Im } b$. We use the following construction

$$\hat{A} \left(\bigvee_{j=0}^{i-1} \hat{A}^{i-1-j} \hat{b}_j \right) \vee_1 \hat{b}_j = \bigvee_{j=0}^i \hat{A}^{i-j} \hat{b}_j,$$

to show that $\hat{A}_* \hat{X}_i \vee_1 \text{Im } \hat{b} \subset \hat{X}_{i+1}$, where $\hat{A}_* \hat{X}_i = \{x | x = \hat{A}x', x' \in \hat{X}_i\}$. Conversely, if $\bigvee_{j=0}^i \hat{A}^{i-j} \hat{b}_j \in \hat{X}_{i+1}$, then

$$\bigvee_{j=0}^i \hat{A}^{i-j} \hat{b}_j = \hat{A} \left(\bigvee_{j=0}^{i-1} \hat{A}^{i-1-j} \hat{b}_j \right) \vee_1 \hat{b}_i.$$

Therefore, $\hat{X}_{i+1} = \hat{A}_* \hat{X}_i \vee_1 \text{Im } \hat{b}$. Then $(\{\hat{X}_i | i \in \mathbb{N}\}, \subset)$ is a chain and contained in the power set $\mathcal{P}(\hat{X})$ of \hat{X} , which is a complete lattice, $(\mathcal{P}(\hat{X}), \subset, \cap, \cup)$, so that the chain has a least upper bound \hat{X}^* , where $\hat{X}^* = \sup\{\hat{X}_i | i \in \mathbb{N}\}$.

The system Σ_2 is reachable if and only if $\hat{X}^* = \hat{X}$. If the system Σ_2 is not reachable, then some nodes in the N^2 diagram cannot be reached. Note that the treatment in [9] assumes a sequence of inputs, and not just an impulse sequence. The set of elements in \hat{X} but not in \hat{X}^* is denoted by \hat{X}/\hat{X}^* , which is the set of unreachable states of the system Σ_2 . Therefore, $X^* \subset \hat{X}^*$, which is an upper bound for X^* . However, if we consider *impulse reachability*, as reachability with an impulse sequence, with \hat{X}_{imp}^* the set of impulse-reachable states, then $X^* = \hat{X}_{\text{imp}}^*$, as shown in the Appendix.

C. Simplified System Model Σ_3

Because the sets B_L^n and \bar{B}_L^n are isomorphic to each other, the system Σ_2 is also isomorphic to the following time-invariant system Σ_3 over B_L ,

$$\bar{x}(k+1) = \bar{A} \bar{x}(k) \vee_2 \bar{b} \bar{u}(k), \quad (5)$$

where the $\bar{A} = \hat{A}$ matrix is the transpose of the truth table of the N^2 diagram of the discrete-event model and $\bar{b} = \hat{b} = b$. The state x is in $\bar{X} = \bar{B}_L^n$ and the control $\bar{u} \in \bar{U} = B_L$. The difference between the system Σ_3 and the system Σ_2 is that the state “strings” \hat{x} in the system Σ_2 have no multiple nonzero entries, but the state \bar{x} in the system Σ_3 can have multiple nonzero entries.

D. Reachability of System Σ_3 and Nodes in the N^2 Diagram

The reachability definition in [9] can also be extended to the system Σ_3 . We are able to find an ascending chain of reachable sets \bar{X}_i . If the least upper bound \bar{X}^* is equal to \bar{X} , then the system Σ_3 is reachable.

Proposition 1: If the following matrix of the system Σ_3 ,

$$C_r(\bar{A}, \bar{b}) = [\bar{b}, \bar{A} \wedge_2 \bar{b}, \dots, \bar{A}^{n-1} \wedge_2 \bar{b}], \quad (6)$$

has any zero rows, then the system Σ_3 is not reachable, i.e. $\bar{X}^* \neq \bar{X}$.

The matrix $C_r(\bar{A}, \bar{b})$ of the system Σ_3 evolves with a finite number of terms, because, if $\bar{X}_{i+1} = \bar{X}_i$, then $\bar{X}_{i+k} = \bar{X}_i$, for all $k = \{1, 2, \dots\}$, and there are only n nodes in the N -squared diagram. Since not only $\hat{A} = \bar{A}$ and $\hat{b} = \bar{b}$, but also \hat{X} is isomorphic to \bar{X} , we can verify that $\hat{X}^* \cong \bar{X}^*$. The reachability problems for these three systems are consistent with each other. The following proposition states this observation.

Proposition 2: If the matrix (6) of the system Σ_3 has zero rows, then both the system Σ_1 and the system Σ_2 are not reachable.

Proposition 3: If the matrix (6) of the system Σ_3 has zero rows, then the row numbers with zero entries are the unreachable node numbers in the N^2 diagram; the row numbers with nonzero entries are the impulse reachable nodes in the N^2 diagram if we assume that the transition conditions can be satisfied at corresponding time instants.

Remark: One must bear in mind that there are two notions here—reachability and impulse reachability. The former assumes a nonzero sequence of inputs, the latter

an impulse input sequence. With respect to the matrix (6), in the former case we think of “linear combinations” of the columns; in the latter case, we consider the columns themselves.

E. Simplified System Model Σ_4

To simplify the computation, we can use the following time-invariant system Σ_4 over the commutative semiring of non-negative real numbers, $(\mathbb{R}^+, +, 0, \times, 1)$,

$$\tilde{x}(k+1) = \tilde{A}\tilde{x}(k) + \tilde{b}\tilde{u}(k), \quad (7)$$

where $x \in \mathbb{R}^{+n}$ and $u \in \mathbb{R}^+$, and the systems matrices (\tilde{A}, \tilde{b}) are the same as system matrices (\bar{A}, \bar{b}) in the system Σ_3 . The following proposition gives us a feasible computational method in Matlab to verify which nodes in the N-squared diagram are always not reachable.

Proposition 4: The matrix (6) of the system Σ_3 and the matrix of the system Σ_4 ,

$$C_r(\tilde{A}, \tilde{b}) = [\tilde{b} \ \tilde{A} \times \tilde{b} \ \dots \ \tilde{A}^{n-1} \times \tilde{b}], \quad (8)$$

have zero rows at the same time.

Proof: See Appendix.

F. Example

This simple example illustrates the results in this section. If we are considering a discrete-event system with the N-squared diagram shown in Fig. 3, then the systems Σ_1 and Σ_2 can be written as Eq. (3) and Eq. (4), where $x_0 = [1, 0, 0, 0, 0]^T$. All $x(1)$, which might be reached, are $[1, 0, 0, 0, 0]^T$ or $[0, 1, 0, 0, 0]^T$. All $x(2)$, which might be reached, are $[1, 0, 0, 0, 0]$, $[0, 1, 0, 0, 0]^T$ or $[0, 0, 0, 1, 0]^T$, so on and so forth. We find that all nodes in the N^2 diagram, which might be reached, are nodes 1, 2 and 4.

	1	2	3	4	5
A	s_1	t_{12}			
B		s_2		t_{24}	
C			s_3		
D	t_{41}			s_4	
E			t_{53}		s_5

Fig. 3. The N-squared diagram of a discrete event system

If we write the system Σ_3 using Eq. (5), where

$$\bar{A} = \begin{pmatrix} 1 & 0 & 0 & 1 & 0 \\ 1 & 1 & 0 & 0 & 0 \\ 0 & 0 & 1 & 0 & 1 \\ 0 & 1 & 0 & 1 & 0 \\ 0 & 0 & 0 & 0 & 1 \end{pmatrix} \text{ and } \bar{b} = \begin{pmatrix} 1 \\ 0 \\ 0 \\ 0 \\ 0 \end{pmatrix},$$

Then matrix $C_r(\bar{A}, \bar{b})$ of the system Σ_3 is

$$C_r(\bar{A}, \bar{b}) = \begin{pmatrix} 1 & 1 & 1 & 1 & 1 \\ 0 & 1 & 1 & 1 & 1 \\ 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 1 & 1 \\ 0 & 0 & 0 & 0 & 0 \end{pmatrix},$$

which has two zero rows. Therefore, the nodes 3 and 5 are not reachable, which is consistent with the systems Σ_1 and Σ_2 . Using Proposition 4, the matrix $C_r(\tilde{A}, \tilde{b})$ of the system Σ_4 with the same $\bar{A} = \tilde{A}$ and $\bar{b} = \tilde{b}$ matrices is

$$C_r(\tilde{A}, \tilde{b}) = \begin{pmatrix} 1 & 1 & 1 & 2 & 5 \\ 0 & 1 & 2 & 3 & 5 \\ 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 1 & 3 & 6 \\ 0 & 0 & 0 & 0 & 0 \end{pmatrix},$$

with the same zero rows, i.e. the nodes 3 and 5 are not reachable. This is also consistent with the conclusion from the system Σ_3 .

VI. CONCLUSION

A state realization of N^2 diagrams over a Boolean semiring provides a means for mathematically analyzing qualitative properties of discrete event control systems (DECS). Of specific interest in this paper is the concept of reachability of linear systems modelled over the Boolean semiring. The generation of analytical qualitative results for such systems is a key feature because rigorous validation of the DECS' reachability is then possible. In particular, such concepts may be developed into a means of analyzing whether a DECS can get into and out of operational modes as required, without the necessity of conducting exhaustive (and potentially damaging in the case of faults and failures) simulations or tests.

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APPENDIX

Proof of $X^* = \hat{X}_{\text{imp}}^*$: Define $N = \{1, 2, \dots, n\}$ as the set of discrete nodes, $O(i)$ as the set of nodes with non-empty entries in the i -th row of the N -squared diagram, i.e. $O(i) = \{j \in N \mid \text{if } j \neq i, \exists t_{ij}; \text{ if } j = i, \exists s_i\}$. If $O(i) = \{j_1, j_2, \dots, j_{n_i}\}$, then the cardinality of $O(i)$ is n_i . Due to the construction of the N^2 diagram, in which node 1 is always the initial active node, $b = \bar{b} = [1, 0, \dots, 0]^T$. All possible states in $x(1)$ of the system Σ_1 under the impulse input sequence are

$$\begin{aligned} x(1) &= \bigvee_{j \in O(1)} j \begin{pmatrix} 0 & 0 & \dots & 0 \\ \vdots & \vdots & \dots & \vdots \\ 1 & 0 & \dots & 0 \\ \vdots & \vdots & \dots & \vdots \\ 0 & 0 & \dots & 0 \end{pmatrix} \begin{pmatrix} 1 \\ \vdots \\ 0 \\ \vdots \\ 0 \end{pmatrix} \vee_1 \begin{pmatrix} 1 \\ \vdots \\ 0 \\ \vdots \\ 0 \end{pmatrix} \\ &= \bigvee_{j \in O(1)} (0, \dots, \alpha_{j1}, \dots, 0)^T, \end{aligned}$$

where $\alpha_{j1} = 1$ if and only if $j \in O(1)$. Notice that node 1 is always a possible active node because there exists a possible self loop in the N^2 diagram. The $\hat{x}(1)$ is

$$\begin{aligned} \hat{x}(1) &= \begin{pmatrix} \hat{a}_{11} & \dots & \hat{a}_{1n} \\ \vdots & \dots & \vdots \\ \hat{a}_{n1} & \dots & \hat{a}_{nn} \end{pmatrix} \begin{pmatrix} 1 \\ \vdots \\ 0 \end{pmatrix} \vee_1 \begin{pmatrix} 1 \\ \vdots \\ 0 \end{pmatrix} \\ &= \bigvee_{j=1}^n (0, \dots, \hat{a}_{j1}, \dots, 0)^T, \end{aligned}$$

where $\hat{a}_{j1} = 1$ if and only if $j \in O(1)$. Therefore, $x(1) = \hat{x}(1)$. By induction, if $x(k) = \hat{x}(k)$, $x(k) = \hat{x}(k) = \bigvee_{i \in I_k} (0, \dots, x_{ik}, \dots, 0)^T$, and $i \in I_k = \{i_1, i_2, \dots, i_{n_k}\} \subset N$ implies $x_{ik} = 1$; otherwise $x_{ik} = 0$. The next state $x(k+1)$ can be computed from $x(k+1) = A(k)x(k) = \bigvee_{i \in I_k} A(k)(0 \dots x_{ik} \dots 0)^T$ and is equal to

$$\begin{aligned} &= \bigvee_{i \in I_k} \bigvee_{j \in O(i)} j \begin{pmatrix} 0 & \dots & 0 & \dots & 0 \\ \vdots & \dots & \vdots & \dots & \vdots \\ 0 & \dots & 1 & \dots & 0 \\ \vdots & \dots & \vdots & \dots & \vdots \\ 0 & \dots & 0 & \dots & 0 \end{pmatrix} \begin{pmatrix} 0 \\ \vdots \\ x_{ik} \\ \vdots \\ 0 \end{pmatrix} \\ &= \bigvee_{i \in I_k} \bigvee_{j \in O(i)} (0, \dots, \alpha_{ji}, \dots, 0)^T, \end{aligned}$$

where $\alpha_{ji} = 1$ if and only if $j \in O(i)$. The next state $\hat{x}(k+1) = \hat{A}\hat{x}(k) = \bigvee_{i \in I_k} \hat{A}(0 \dots x_{ik} \dots 0)^T$ is equal to

$$\bigvee_{i \in I_k} \begin{pmatrix} \hat{a}_{11} & \dots & \hat{a}_{1i} & \dots & \hat{a}_{1n} \\ \vdots & \dots & \vdots & \dots & \vdots \\ \hat{a}_{j1} & \dots & \hat{a}_{ji} & \dots & \hat{a}_{jn} \\ \vdots & \dots & \vdots & \dots & \vdots \\ \hat{a}_{n1} & \dots & \hat{a}_{ni} & \dots & \hat{a}_{nn} \end{pmatrix} \begin{pmatrix} 0 \\ \vdots \\ x_{ik} \\ \vdots \\ 0 \end{pmatrix}$$

$$= \bigvee_{i \in I_k} \bigvee_{j \in O(i)} (0, \dots, \hat{a}_{ji}, \dots, 0)^T$$

where $\hat{a}_{ji} = 1$ if and only if $j \in O(i)$. Therefore, we have that $X^* = \hat{X}_{\text{imp}}^*$. \diamond

Proof of Proposition 4: We will prove that each column of the matrices (6) and (8) has zero rows at the same time. Starting from the 1-st column, because $\tilde{b} = \bar{b} = [1, 0, \dots, 0]^T$, they have the same zero rows. Since $\bar{A} = \tilde{A}$, we examine the second column $\bar{A} \wedge_2 \bar{b}$ and $\tilde{A} \times \tilde{b}$ and obtain that $\bar{A} \wedge_2 \bar{b} = \tilde{A} \times \tilde{b}$ which is equal to

$$\begin{pmatrix} \bar{a}_{11} & \dots & \bar{a}_{1n} \\ \vdots & \dots & \vdots \\ \bar{a}_{n1} & \dots & \bar{a}_{nn} \end{pmatrix} \begin{pmatrix} 1 \\ \vdots \\ 0 \end{pmatrix} = \begin{pmatrix} \bar{a}_{11} \\ \vdots \\ \bar{a}_{n1} \end{pmatrix}.$$

Therefore, they have zero rows at the same time. By induction, if $\bar{A}^k \wedge_2 \bar{b}$ and $\tilde{A}^k \times \tilde{b}$ have zero rows at the same time, then we need to show that $\bar{A}^{k+1} \wedge_2 \bar{b}$ and $\tilde{A}^{k+1} \times \tilde{b}$ also have zero rows at the same time. Assume $\bar{A}^k \wedge_2 \bar{b} = (\bar{b}_{1k}, \dots, \bar{b}_{nk})$ and $\tilde{A}^k \times \tilde{b} = (\tilde{b}_{1k}, \dots, \tilde{b}_{nk})$. The i -th rows, where $i \in I_k = \{i_1, \dots, i_{n_k}\} \subset N$, of the vector $\bar{A}^k \wedge_2 \bar{b}$ and $\tilde{A}^k \times \tilde{b}$ are both non-zero; otherwise the rows are both zero. Then $\bar{A}^{k+1} \wedge_2 \bar{b} = \bar{A} \bigvee_{i \in I_k} (0 \dots \bar{b}_{ik} \dots 0)^T =$

$\bigvee_{i \in I_k} \bar{A} (0 \dots \bar{b}_{ik} \dots 0)^T$ which is equal to

$$\begin{aligned} &\bigvee_{i \in I_k} \begin{pmatrix} \bar{a}_{11} & \dots & \bar{a}_{1i} & \dots & \bar{a}_{1n} \\ \vdots & \dots & \vdots & \dots & \vdots \\ \bar{a}_{j1} & \dots & \bar{a}_{ji} & \dots & \bar{a}_{jn} \\ \vdots & \dots & \vdots & \dots & \vdots \\ \bar{a}_{n1} & \dots & \bar{a}_{ni} & \dots & \bar{a}_{nn} \end{pmatrix} \begin{pmatrix} 0 \\ \vdots \\ \bar{b}_{ik} \\ \vdots \\ 0 \end{pmatrix} \\ &= \bigvee_{i \in I_k} (\bar{a}_{1i} \vee \bar{b}_{ik}, \dots, \bar{a}_{ji} \vee \bar{b}_{ik}, \dots, \bar{a}_{ni} \vee \bar{b}_{ik})^T \\ &= \bigvee_{i \in I_k} (\bar{a}_{1i}, \dots, \bar{a}_{ji}, \dots, \bar{a}_{ni})^T, \end{aligned}$$

where \bar{a}_{ji} is non-zero if and only if $j \in O(i)$; $\tilde{A}^{k+1} \times \tilde{b} = \tilde{A} \sum_{i \in I_k} (0 \dots \tilde{b}_{ik} \dots 0)^T = \sum_{i \in I_k} \tilde{A} (0 \dots \tilde{b}_{ik} \dots 0)^T$ which is equal to

$$\begin{aligned} &\sum_{i \in I_k} \begin{pmatrix} \tilde{a}_{11} & \dots & \tilde{a}_{1i} & \dots & \tilde{a}_{1n} \\ \vdots & \dots & \vdots & \dots & \vdots \\ \tilde{a}_{j1} & \dots & \tilde{a}_{ji} & \dots & \tilde{a}_{jn} \\ \vdots & \dots & \vdots & \dots & \vdots \\ \tilde{a}_{n1} & \dots & \tilde{a}_{ni} & \dots & \tilde{a}_{nn} \end{pmatrix} \begin{pmatrix} 0 \\ \vdots \\ \tilde{b}_{ik} \\ \vdots \\ 0 \end{pmatrix} \\ &= \sum_{i \in I_k} (\tilde{a}_{1i} \times \tilde{b}_{ik}, \dots, \tilde{a}_{ji} \times \tilde{b}_{ik}, \dots, \tilde{a}_{ni} \times \tilde{b}_{ik})^T, \end{aligned}$$

where $\tilde{a}_{ji} \times \tilde{b}_{ik} \neq 0$ if and only if $j \in O(i)$. Thus, $\bar{A}^{k+1} \wedge_2 \bar{b}$ and $\tilde{A}^{k+1} \times \tilde{b}$ have zero or non-zero rows at the same time. Thus, the two matrices $C_r(\bar{A}, \bar{b})$ and $C_r(\tilde{A}, \tilde{b})$ have the zero or non-zero rows at the same time. \diamond