A Continuous-Time Formulation for Scheduling Multi-Stage Multi-product Batch Plants with Non-identical Parallel Units

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Abstract
Multi-stage multi-product batch plants with non-identical parallel units are quite common in the batch chemical industry. Scheduling production in these plants optimally is a complex task that has received limited attention in the literature. In this work, we propose a new continuous time-representation using asynchronous slots and report a mixed integer linear programming (MILP) formulation for the short-term scheduling of such plants. Using several examples, we show that our formulation is simpler, tighter, and performs better than previous formulations.

Keywords: MILP; multi-product; batch plant; scheduling; makespan

1. Introduction
Scheduling of plant operations is a routine activity in all batch plants. Although the task of optimal scheduling is complex in even simpler batch plant configurations such as serial multi-product process, it is even more difficult in the case of multi-stage multi-product batch plants with non-identical parallel units. However, in spite of their industrial significance, the existing research has paid little attention to the scheduling in plants with multiple stages.

Pinto and Grossmann (1995) developed a continuous-time, slot-based MILP formulation for the short-term scheduling of multistage batch plants. They assumed sequence-independent set-ups and unconstrained resources. To assign orders to the time slots on a unit, they used tetra-index (order, slot, unit, and stage) binary variables. This increased significantly the number of binary variables for larger problems. Hui, Gupta, and Meulen (2000) presented a formulation with a set of tri-index binary variables (order, order, stage) instead of the tetra-index binary variables, i.e. (order, order, stage, unit). Although they could deal with sequence-dependent set-up times, they could solve problems with only five orders to optimality in reasonable time. Recently, Gupta and Karimi (2003) developed several improved sequence-based MILP formulations. In contrast to Pinto and Grossmann (1995), they used tri-index variables (order, order, and stage) to handle order-sequence dependencies explicitly. Compared to other sequence-

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based formulations in the literature, their formulations used fewer binary variables and constraints, solved faster, and gave better objective values in some cases. All these formulations are still not suitable for larger problems, and more effective formulations and algorithms are badly needed for this difficult problem.

In this paper, we develop a more effective approach for this tough scheduling problem. We begin this paper with a detailed problem description and then develop an MILP formulation for this problem. Next, we compare our model with the existing models to demonstrate the superiority of our model with three examples.

2. Problem Description

Figure 1 shows a multi-stage multi-product batch process with non-identical parallel units. The plant has $S$ batch stages ($s = 1, 2, \ldots, S$) and a total of $J$ batch units ($j = 1, 2, \ldots, J$). We define set $U_s = \{ j \mid j \text{ is in stage } s \}$ and $m_s = |U_s|$, i.e. $J = m_1 + m_2 + \ldots + m_S$. We assume that unlimited intermediate storage (UIS) exists between consecutive processing stages.

The plant operates in a batch mode, i.e. produces individual batches of products rather than long campaigns of identical batches. Let $I$ denote the number of batches ($i = 1, 2, \ldots, I$) that the plant must process. Some of these batches may be identical. We assume that each batch follows the sequence $1, 2, \ldots, S$ of stages for processing. If a batch skips a specific stage, then its processing time is zero on all units in that stage. When parallel units are non-identical, a unit may not be able to process all batches. To accommodate these preferences, we define $I_i = \{ i \mid \text{batch } i \text{ can be processed on unit } j \}$ and $J_i = \{ j \mid \text{unit } j \text{ can process batch } i \}$. A real scheduling problem could involve tens of batches, 2-5 stages, and 20-30 batch units.

In addition to the above process features, we assume the following.
1. A unit cannot process more than one batch at a time.
2. Processing is non-preemptive.
3. Processing units do not fail and processed batches are always satisfactory.
4. Start of the current scheduling period is zero time.
5. Neglect transition times.
6. The size of each batch is known a priori.
7. More than one unit cannot process a single batch.
8. Resources are unlimited.

With these, we can state a short-term scheduling problem for the above process as follows. Given \( J_s, I_j, U_s \), and the processing times of batches on suitable units, identify the units that should process each batch and the start/end times of all batches on each unit to optimize some scheduling criterion. We use makespan as the scheduling objective.

### 3. MILP Formulation

The main feature of our slot-based formulation is that we define time slots for each stage and each unit separately. Instead of assigning batches to unit-slots, we assign them to stage-slots and then derive their assignments to specific units later. By doing so, we reduce the number of binary variables dramatically compared with the existing slot-based formulations.

We represent time (Figure 2) on each stage \( s \) and each unit \( j \) by exactly \( I \) contiguous, variable-length slots \( (k = 1, 2, \ldots, I) \). Note that the unit-slots for a stage are asynchronous with their stage-slots and stage-slots are asynchronous across stages. Let \( T_{ks} \) be the time at which slot \( k \) ends on stage \( s \) and \( t_{jk} \) be the time at which it ends on unit \( j \). As shown in Figure 2, the endpoint of each stage-slot matches with the endpoint of exactly one slot of some unit in that stage. For instance, \( T_{3s} = t_{23} \) in Figure 2. For each stage-slot, we use the following binary variable and constraint to assign exactly one batch, which must exit the stage at the end of that slot.

\[
\begin{align*}
\gamma_{es} & = \begin{cases} 
1 & \text{if batch } i \text{ exits stage } s \text{ at time } T_{es} \\
0 & \text{otherwise}
\end{cases}
\end{align*}
\]
Since a batch must pass through each stage once and only once, we have,
\[ \sum_i y_{i,s} = 1 \]  \hspace{1cm} (1)

Having assigned a batch to each stage-slot, we now assign a unit to each batch by defining the following binary variable.
\[ z_{ij} = \begin{cases} 1 & \text{if unit } j \text{ processes batch } i \\ 0 & \text{otherwise} \end{cases} \quad j \in U_s \]  \hspace{1cm} (2)

Note that the index \( s \) in the above is not free. Fixing \( j \) fixes \( s \), as \( j \in U_s \). Since only one unit in each stage must process a given batch, we write the following for each stage \( s \),
\[ \sum_{j \in U_s} z_{ij} = 1 \]  \hspace{1cm} (3)

Using the two binary variables \( y_{i,s} \) and \( z_{ij} \), we define a 0-1 continuous variable \( YZ_{ijk} = y_{i,s} z_{ij} \). We linearize this by using,
\[ \sum_{j \in U_s, i \in I} YZ_{ijk} = y_{i,s} \]  \hspace{1cm} (4a)
\[ \sum_{i \in I_k} YZ_{ijk} = z_{ij} \quad j \in U_s, j \in J_i \]  \hspace{1cm} (4b)

Having assigned batches to slots and units, we now determine the completion times of batches and slot times. First, slots on each stage are contiguous and ordered, so
\[ T_k \geq T_{k-1} \quad T_{k0} = 0 \]  \hspace{1cm} (5)

Note that \( YZ_{ijk} = 1 \), only when a batch \( i \) exits a unit \( j \) at some slot \( k \) (or time \( t_{jk} \)), otherwise it is zero. Therefore, to ensure that the processing of every batch that exits a unit \( j \) at \( t_{jk} \), we use,
\[ t_{jk} \geq t_{j,k-1} + \sum_{i \in I} PT_{ij} YZ_{ijk} \quad j \in U_s, t_{j0} = URT_j \]  \hspace{1cm} (6)

Where, \( URT_j \) is the release time of unit \( j \), which is the earliest time at which unit \( j \) is ready to accept a new batch. Note that if a batch does not exit a unit \( j \) at \( t_{jk} \), then \( t_{jk} = t_{jk-1} \). Thus, only the unit-slot from which a batch leaves provides the processing time \( (PT_{ij}) \). Now, if a batch does leave a unit \( j \) at \( t_{jk} \), then we must register this exit on the time axis of the corresponding stage as well by forcing \( T_{ks} = t_{jk} \). We do this as follows.
\[ T_{k} \geq t_{jk} \quad j \in U_s \]  \hspace{1cm} (7a)
\[ T_{k} \leq t_{jk} + M(1 - \sum_{i \in I} YZ_{ijk}) \quad j \in U_s \]  \hspace{1cm} (7b)

Where, \( M \) is a constant larger than the maximum possible makespan. Having ensured that each batch gets sufficient time for processing on a suitable unit, we must ensure that a stage \( s \) processes a batch only after stage \((s-1)\) has completed it. To this end, we
define $CT_i$ as the completion time of batch $i$ on stage $s$, i.e. the time at which it exits stage $s$. Clearly, this is the time at which the stage-slot to which the batch is assigned ends, therefore,

$$CT_s = \sum_i T_{is} y_{is}$$

Then, to ensure that a batch processes on a stage only after it has finished processing on the previous stage, we require,

$$CT_s \geq CT_{i(i-1)} + \sum_{j \in U_s} PT_{is} z_{is}$$

$$CT_s = BRT_i$$

where $BRT_i$ is the release time of batch $i$.

Next, we define $TYE_{iks} = T_{iks} ye_{iks}$ to linearize the nonlinear expression for $CT_i$ with the constraints below.

$$CT_i \geq T_{iks} - M(1 - ye_{iks})$$

$$CT_i \leq T_{iks} + M(1 - ye_{iks})$$

Lastly, we minimize the makespan ($H$) given by any one of the following constraints. Although it is sufficient to use just one constraint and most of the following constraints are redundant, we found that using all three reduced the solution time.

$$H \geq T_{iks}$$

$$H \geq CT_i$$

$$H \geq t_{jk}$$

4. Model Evaluation

To evaluate this formulation and compare it with those of Pinto and Grossmann (1995) and Gupta and Karimi (2002), we use three examples. Example 1 has two batch stages ($S = 2$) and process nine batches ($I = 9$). Stage 1 has two parallel units ($U_1 = \{1, 2\}$ and $m_1 = 2$) and stage 2 has three ($U_2 = \{3, 4, 5\}$ and $m_2 = 3$). Table 1 gives the remaining data for this example.

<table>
<thead>
<tr>
<th>Batch $(i)$</th>
<th>Unit $(j)$</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>2 3 4 5</td>
</tr>
<tr>
<td>2</td>
<td>15 5 62 44 33</td>
</tr>
<tr>
<td>3</td>
<td>2 20 42 70</td>
</tr>
<tr>
<td>4</td>
<td>16 17 50 41</td>
</tr>
<tr>
<td>5</td>
<td>16 37 64 24</td>
</tr>
<tr>
<td>6</td>
<td>12 8 53 34 21</td>
</tr>
<tr>
<td>7</td>
<td>4 19 21 37 23</td>
</tr>
<tr>
<td>8</td>
<td>18 17 24 43 39</td>
</tr>
<tr>
<td>9</td>
<td>11 2 10 34 36</td>
</tr>
</tbody>
</table>
For solving the three exams, we used CPLEX 9.0 in GAMS 21.4 on an hp workstation xw6200 (Intel Xeon 3.40 GHz CPU and 3.00 GB RAM) running Windows XP. Table 2 shows the performances of and statistics for various models. Our model clearly performs much better than the existing two models. From Table 2, we see that only our model finds the optimal solution within 1000 s for all examples. Furthermore, our model also gives higher RMIP values for all the problems, which indicates that our formulation can be tighter than the other two models. In spite of using more binary variables, our model is much faster than the model of Gupta and Karimi (2003). Compared to the slot-based model of Pinto and Grossmann (1995), our model uses almost half the binary variables.

<p>| Table 2. Comparison of our model with two literature models for the examples |
|---------------------------------|--------|--------|--------|--------|--------|--------|--------|--------|</p>
<table>
<thead>
<tr>
<th>Example 1 (S=2, m₁=2, m₂=3, I=9)</th>
<th>Model</th>
<th>Binary Variable</th>
<th>Non-zeros</th>
<th>Variables</th>
<th>Constraints</th>
<th>RMIP (h)</th>
<th>CPU Time (s)</th>
<th>Relative Gap (%)</th>
<th>MIP (h)</th>
<th>Nodes</th>
</tr>
</thead>
<tbody>
<tr>
<td>Ours</td>
<td>207</td>
<td>3669</td>
<td>694</td>
<td>832</td>
<td>64.11</td>
<td>146</td>
<td>0</td>
<td>84</td>
<td>35390</td>
<td></td>
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<tr>
<td>Gupta</td>
<td>152</td>
<td>2456</td>
<td>209</td>
<td>613</td>
<td>41</td>
<td>1000</td>
<td>16.67</td>
<td>84</td>
<td>409994</td>
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<tr>
<td>Pinto</td>
<td>360</td>
<td>3524</td>
<td>473</td>
<td>838</td>
<td>41</td>
<td>1000</td>
<td>0.53</td>
<td>84</td>
<td>619954</td>
<td></td>
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</tr>
<tr>
<td>Example 2 (S=2, m₁=3, m₂=4, I=8)</td>
<td>Ours</td>
<td>184</td>
<td>3633</td>
<td>721</td>
<td>770</td>
<td>22.65</td>
<td>23</td>
<td>0</td>
<td>34</td>
<td>5244</td>
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<tr>
<td>Gupta</td>
<td>168</td>
<td>3112</td>
<td>241</td>
<td>743</td>
<td>22</td>
<td>1000</td>
<td>17.65</td>
<td>34</td>
<td>51720</td>
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<tr>
<td>Pinto</td>
<td>504</td>
<td>4908</td>
<td>649</td>
<td>1154</td>
<td>22</td>
<td>588</td>
<td>0</td>
<td>34</td>
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</tr>
<tr>
<td>Example 3 (S=3, m₁=3, m₂=2, m₃=5, I=8)</td>
<td>Ours</td>
<td>272</td>
<td>5273</td>
<td>1041</td>
<td>1134</td>
<td>103.83</td>
<td>72</td>
<td>0</td>
<td>148</td>
<td>10646</td>
</tr>
<tr>
<td>Gupta</td>
<td>248</td>
<td>4504</td>
<td>353</td>
<td>1082</td>
<td>66</td>
<td>1000</td>
<td>32.21</td>
<td>149</td>
<td>398600</td>
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</tr>
<tr>
<td>Pinto</td>
<td>720</td>
<td>7016</td>
<td>929</td>
<td>1652</td>
<td>66</td>
<td>194</td>
<td>0</td>
<td>148</td>
<td>36881</td>
<td></td>
</tr>
</tbody>
</table>

5. Conclusion

An MILP formulation using a novel continuous-time asynchronous slot-based time representation was developed for scheduling multi-stage multi-product batch plants with non-identical parallel units. Evaluation on three examples shows that our formulation is simpler, tighter, and at least two times faster than the best existing formulations.

References

